

	1	2	3	4	5	6	7	8																																
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DATE	CHANGE NO.	REV																																						
	1	2	3	4	5	6	7	8																																

ENVY

MV BUILD

2009.0729

DRAWER	EE	DATE	POWER	DATE	INVENTEC	
DESIGN						
CHECK						
RESPONSIBLE						
SIZE = 3 VER :					TITLE	ENVY
FILE NAME : XXXX XXXXXX-XX					SIZE	A3
P/N XXXXXXXXXXXX					CODE	CS
					DOC. NUMBER	1310A22315-0-MTR
					REV	A04
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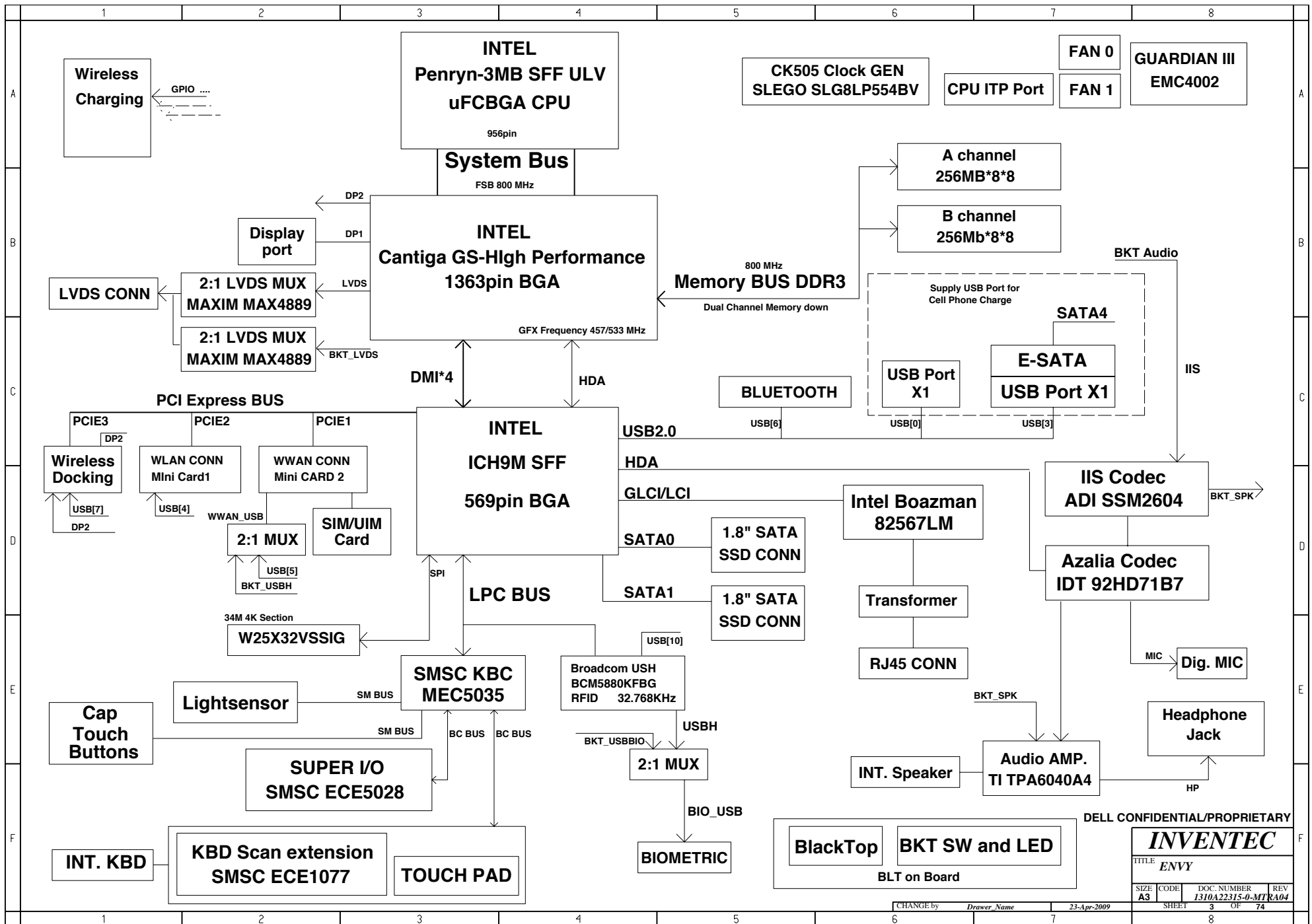
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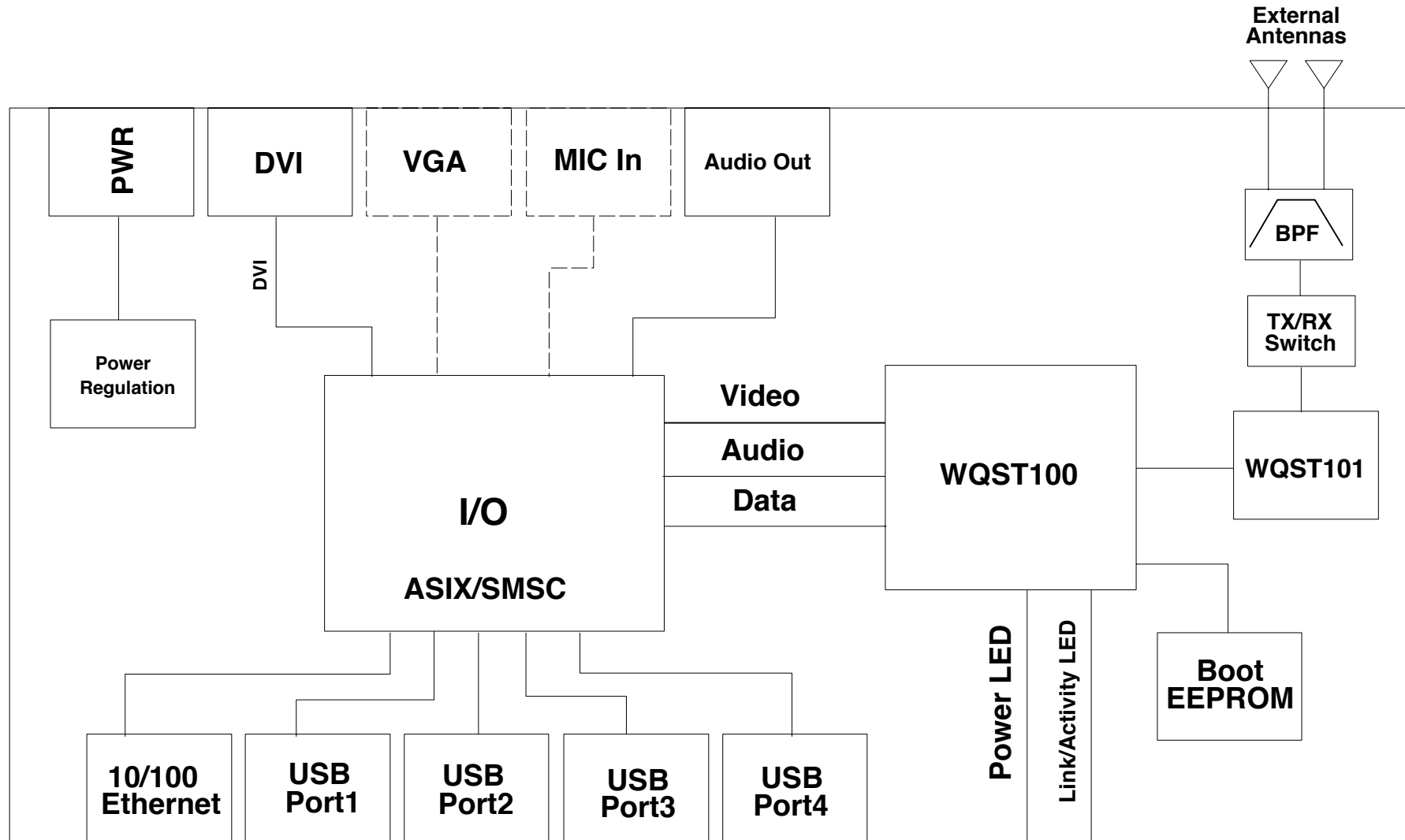
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SIZE	CODE	DOC. NUMBER	REV
A3	CS	1310A22315-0-MTR	A04

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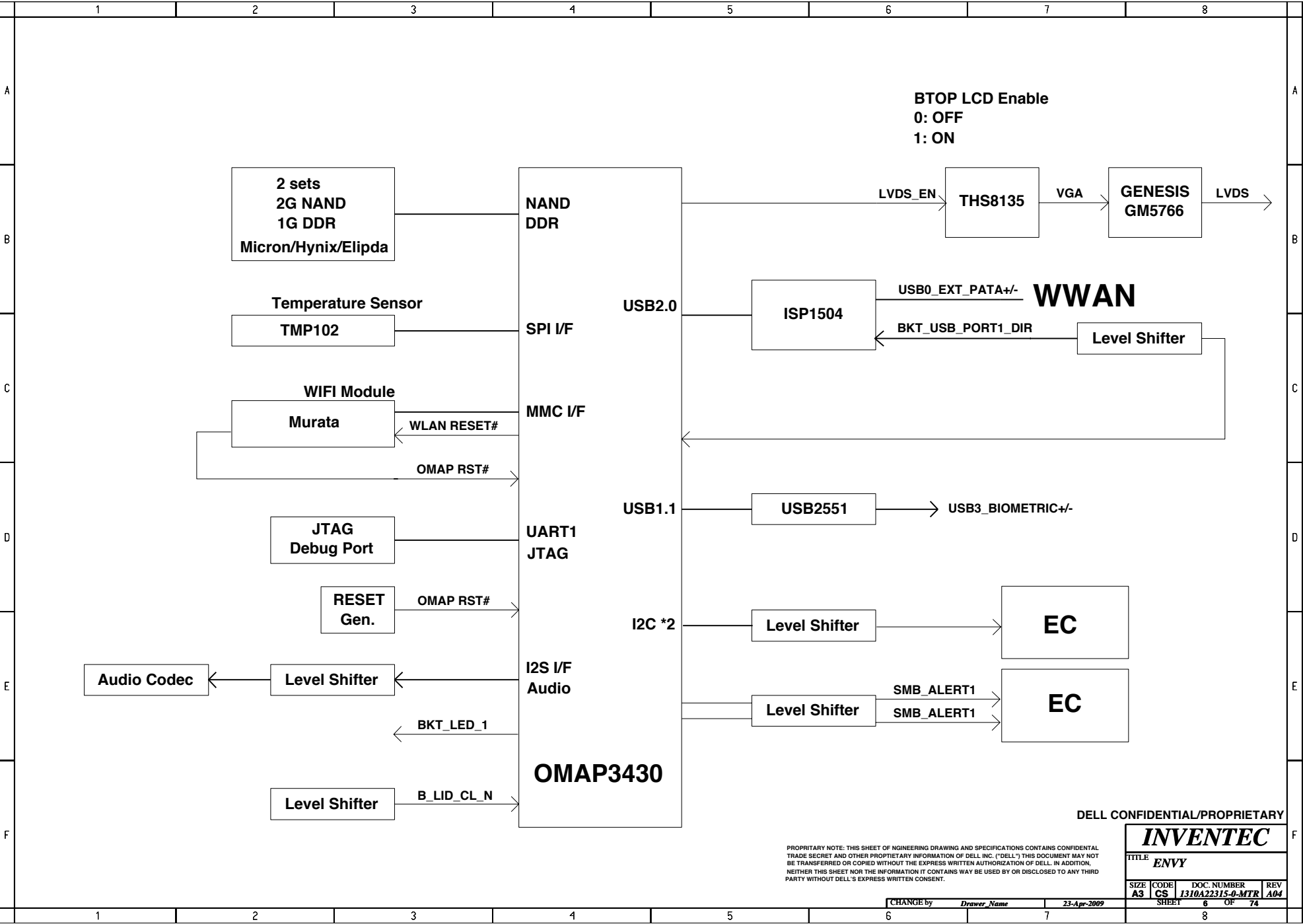


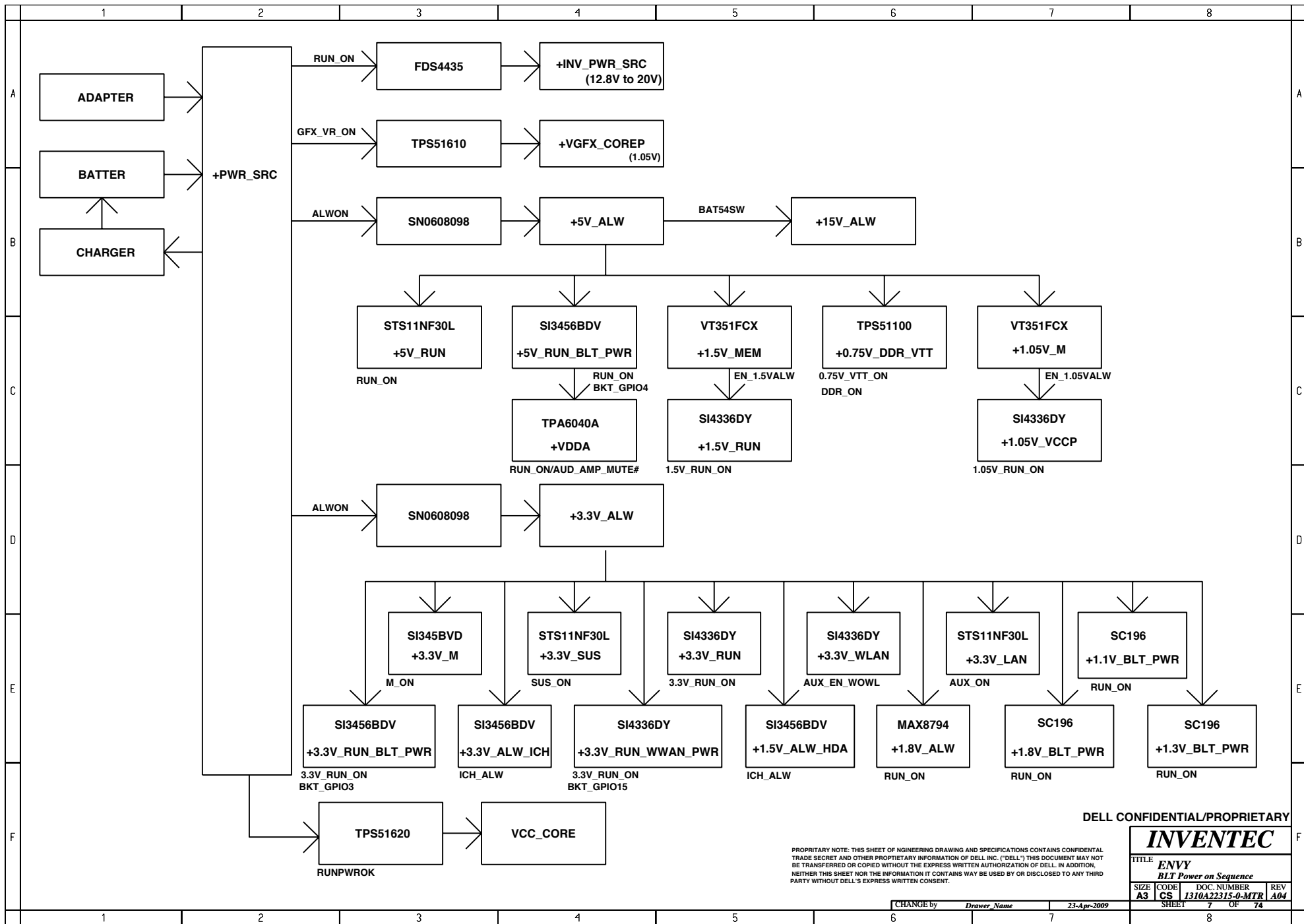
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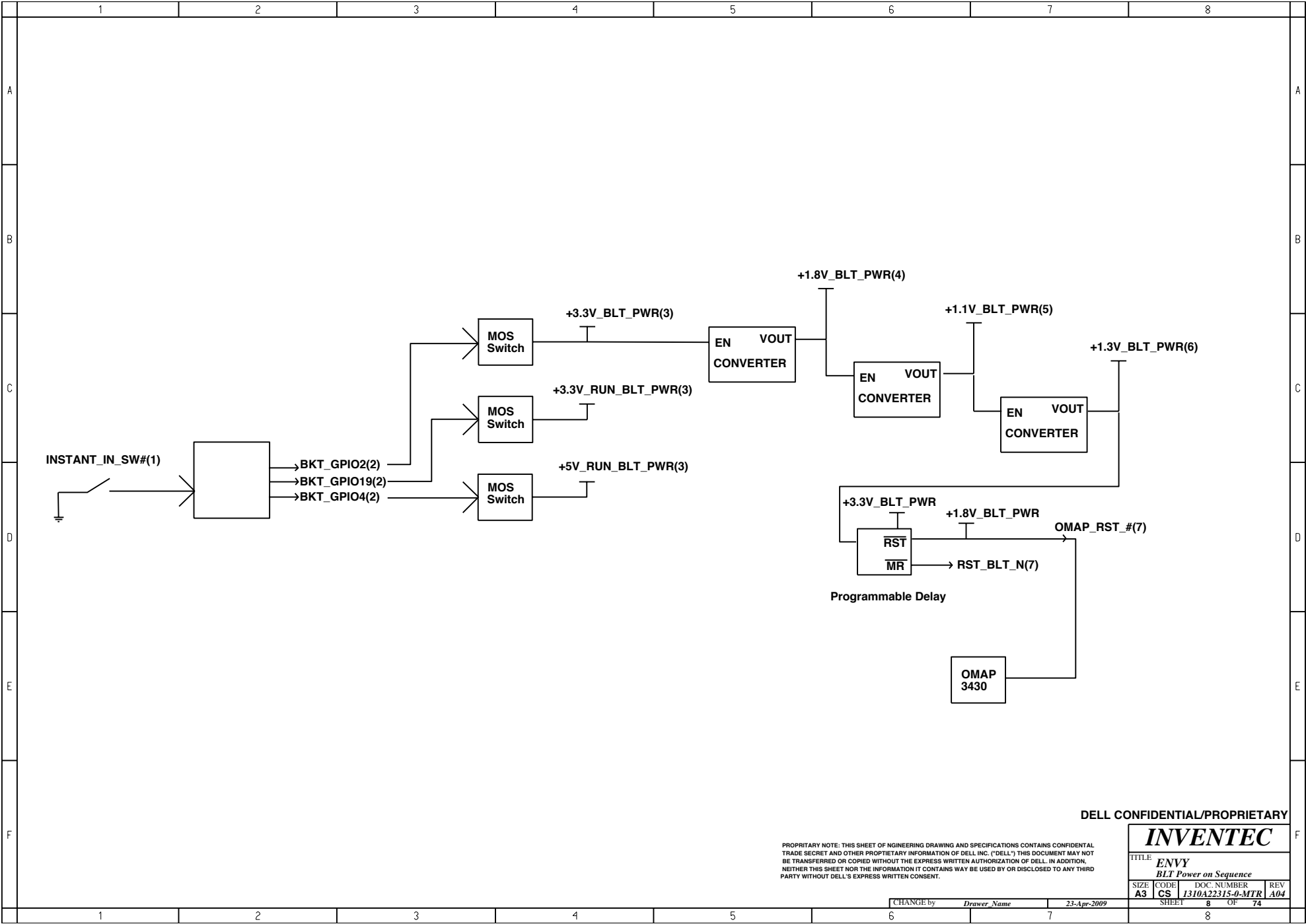
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INVENTEC			
TITLE: ENVY Wireless Docking			
SIZE: A3	CODE: CS	DOC. NUMBER: 1310A22315-0-MTR	REV: A04
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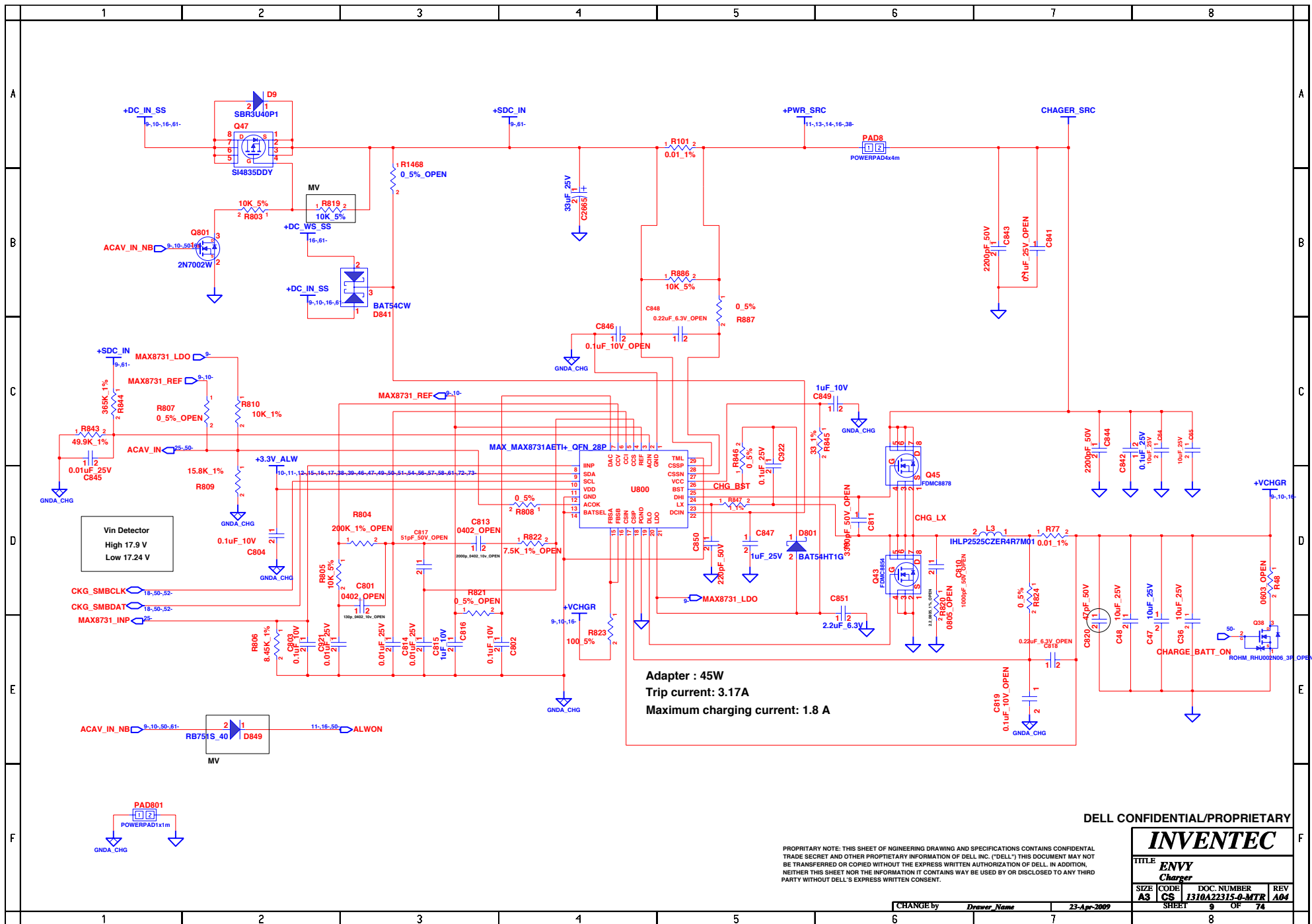
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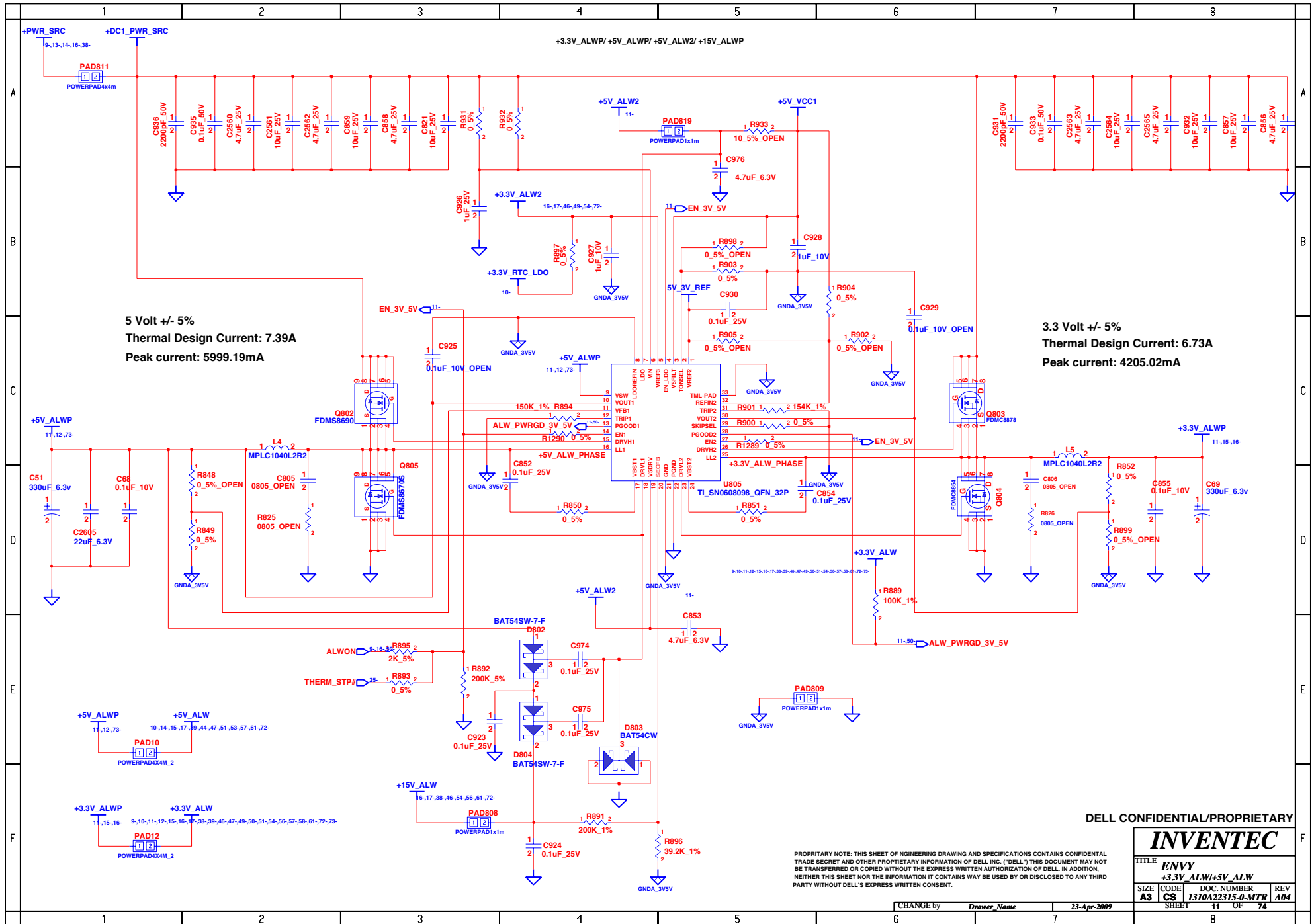
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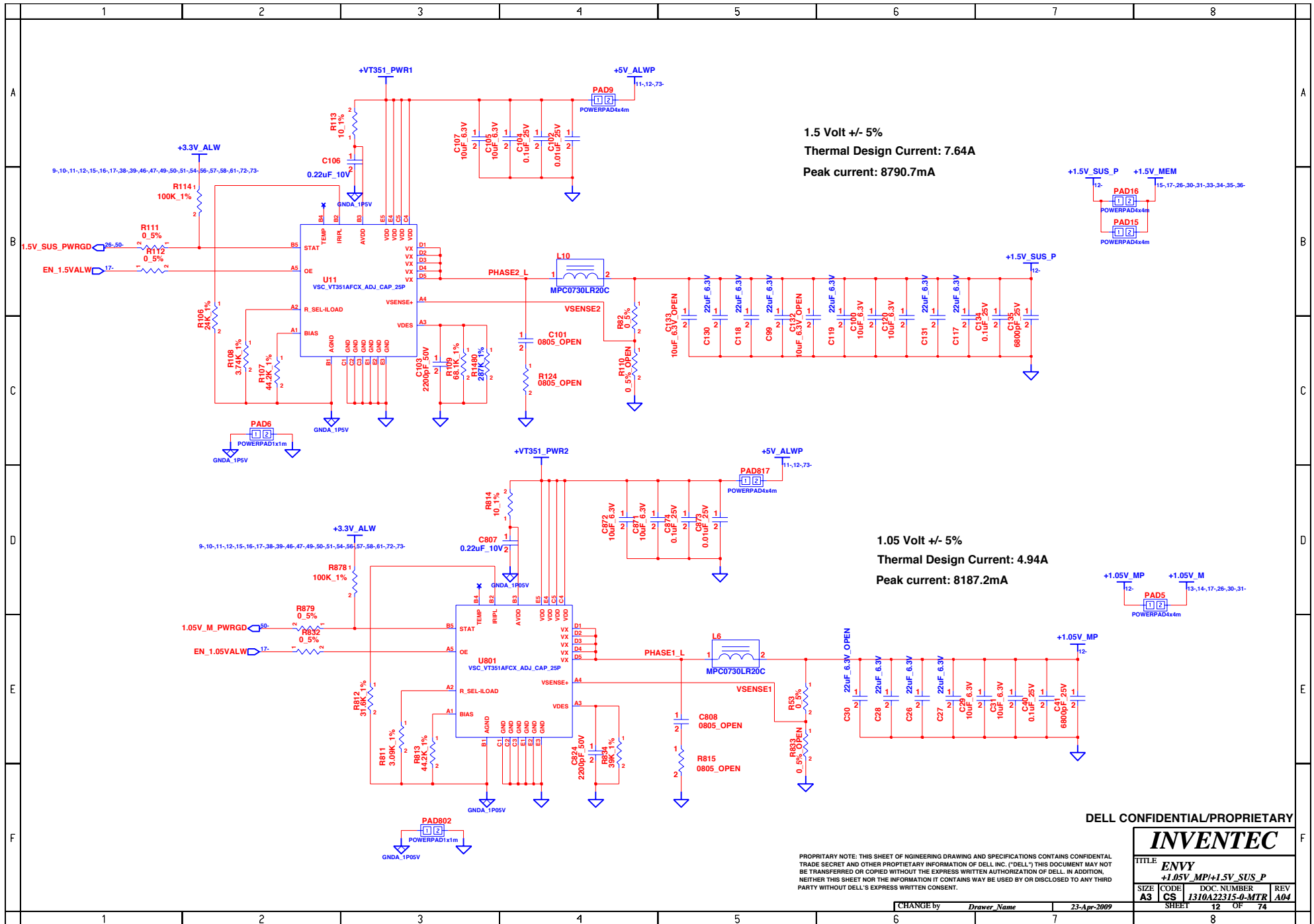
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SHEET: 8		OF: 74	

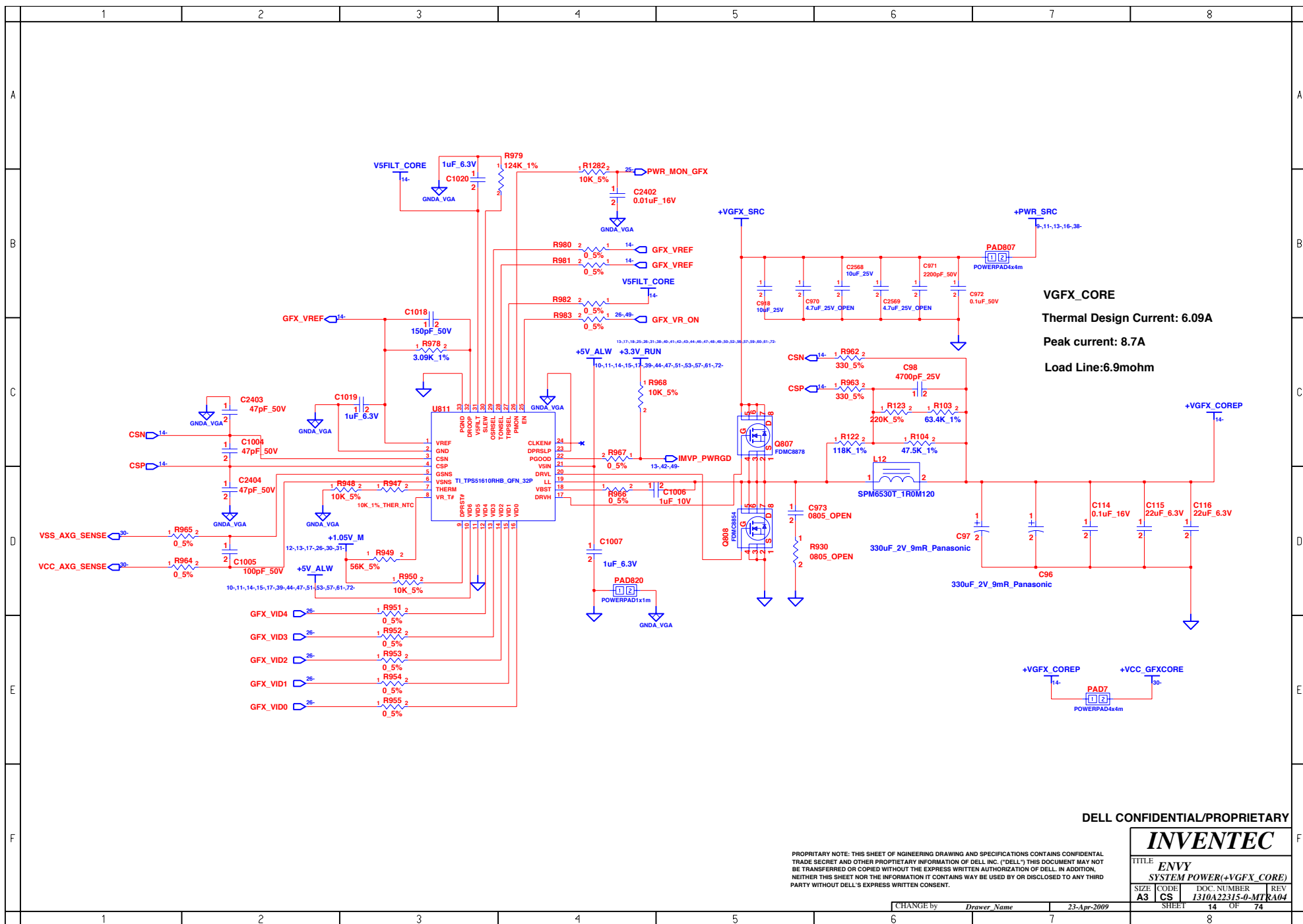
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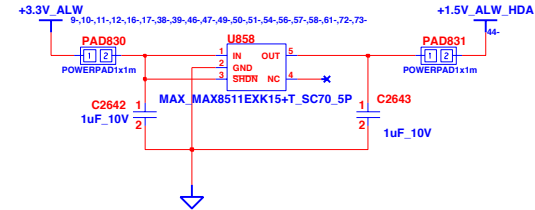
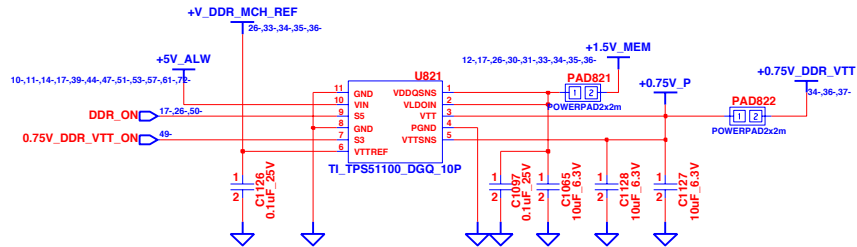




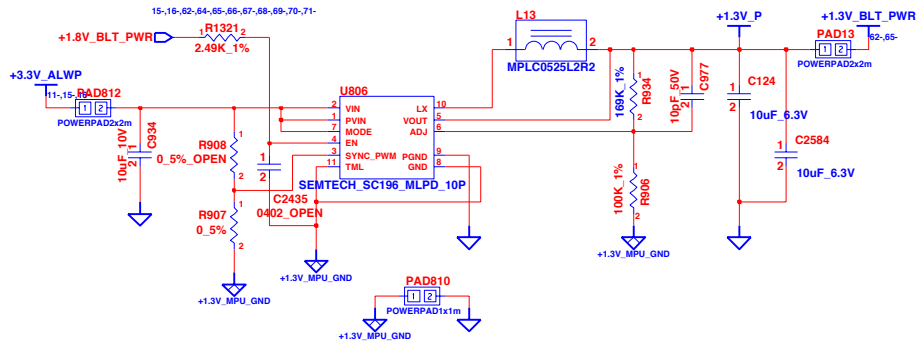




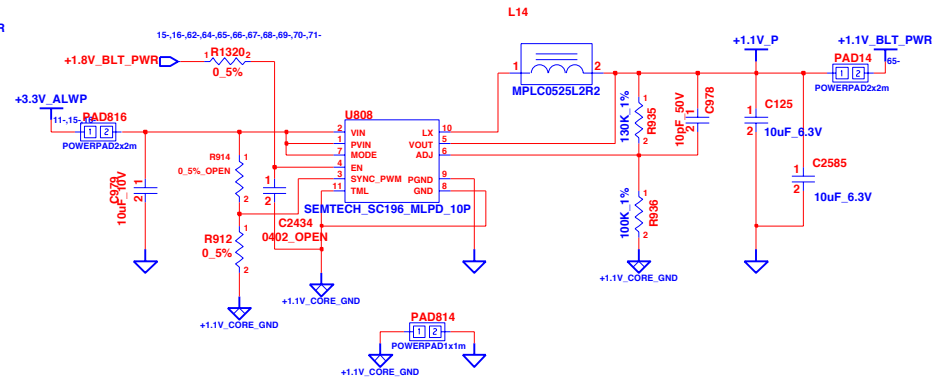
0.75 Volt DDR_VTT
Peak current: 1500mA



Blacktop Power +1.3V_BLT_PWRV
Peak current: 1380mA



Blacktop Power +1.1V_BLT_PWRV
Peak current: 500mA



non-pop in non-BLT sku

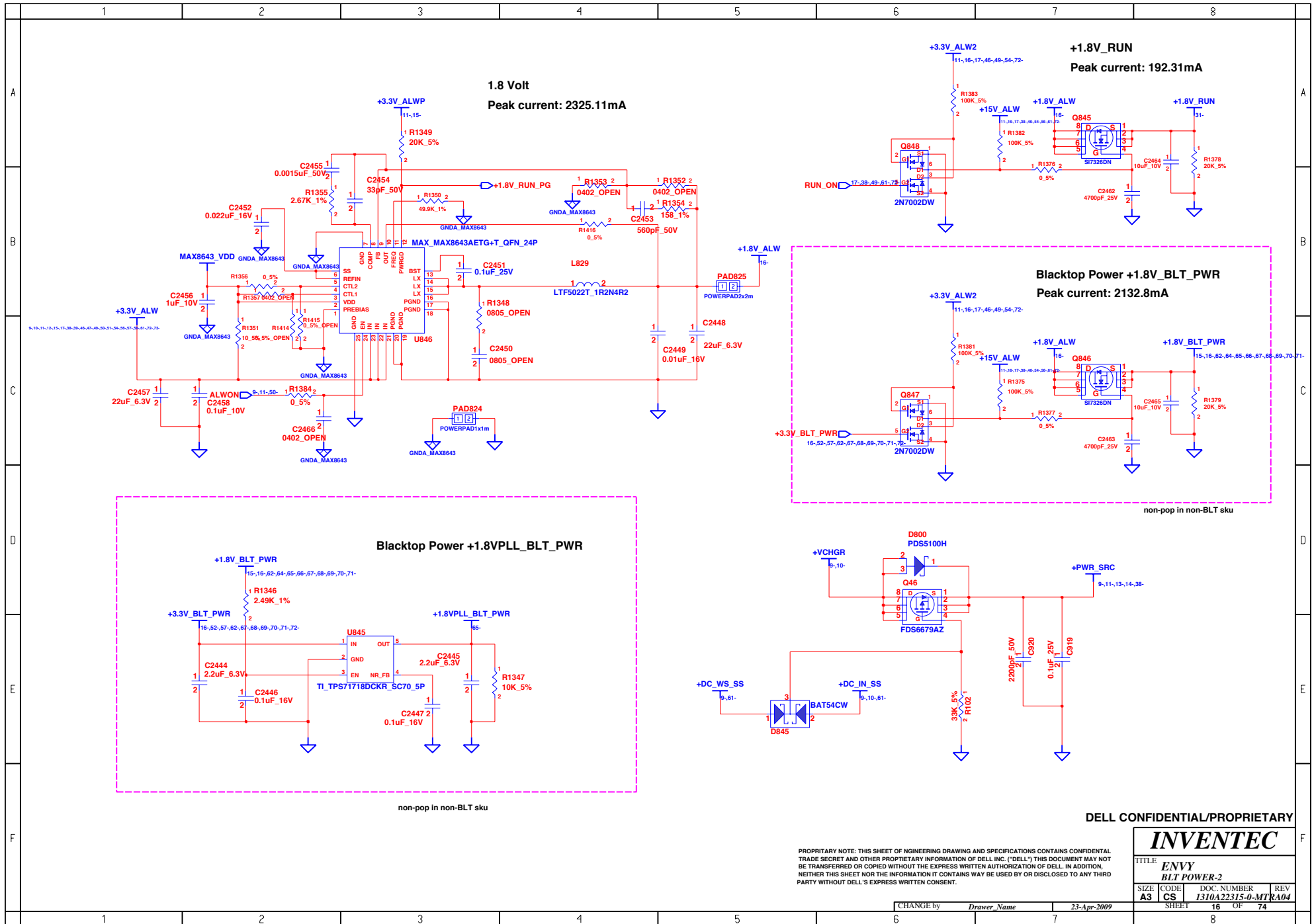
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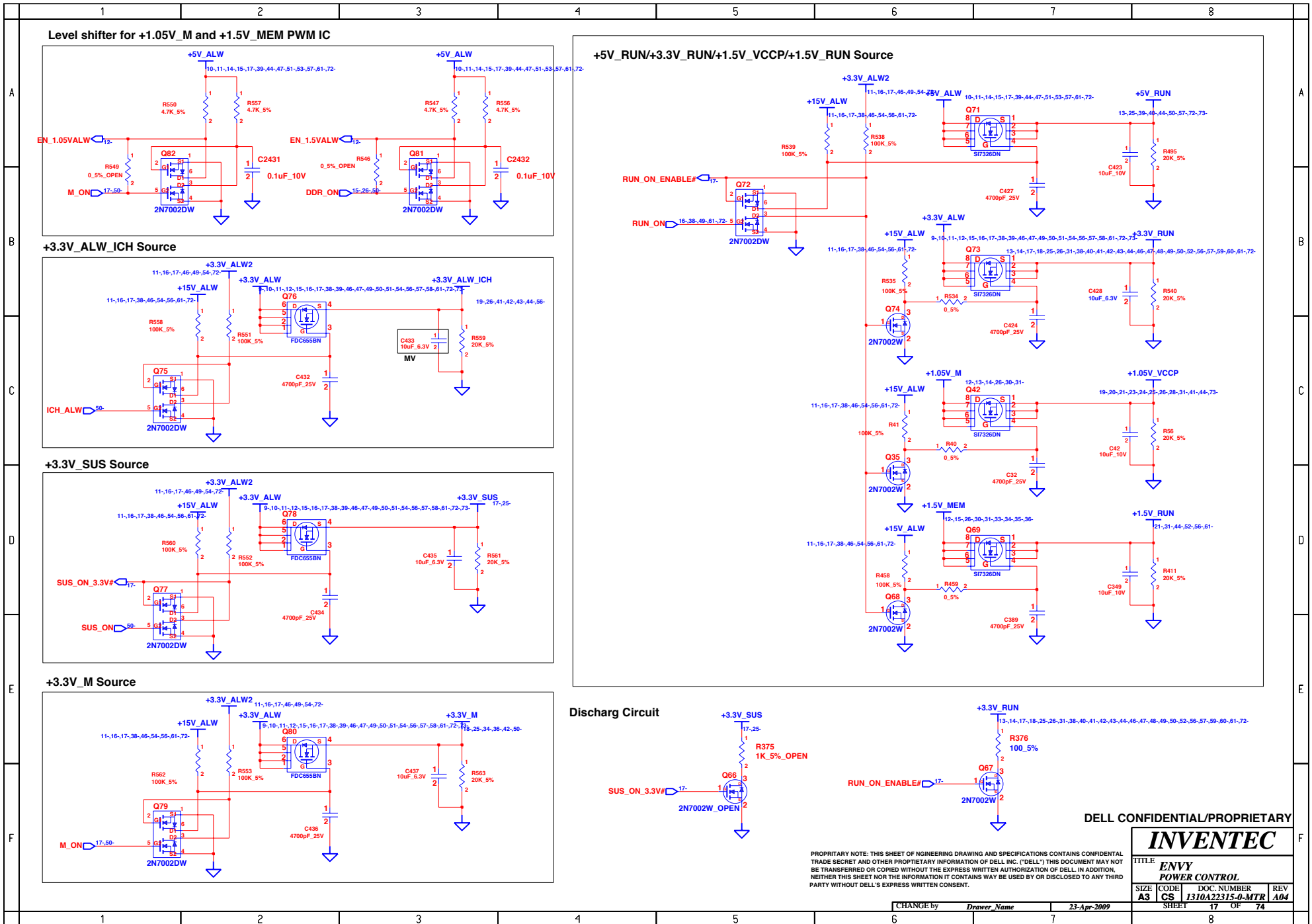
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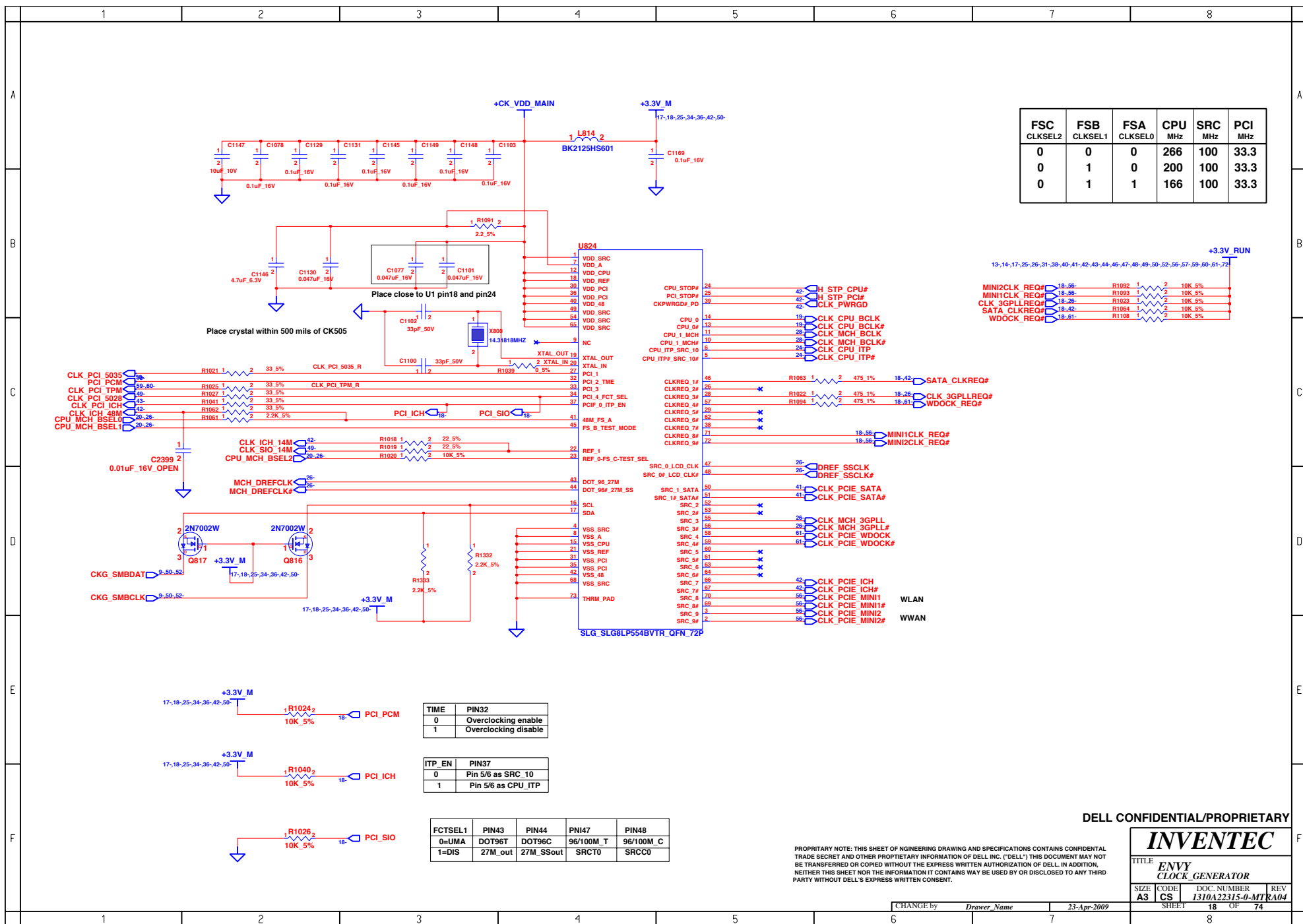
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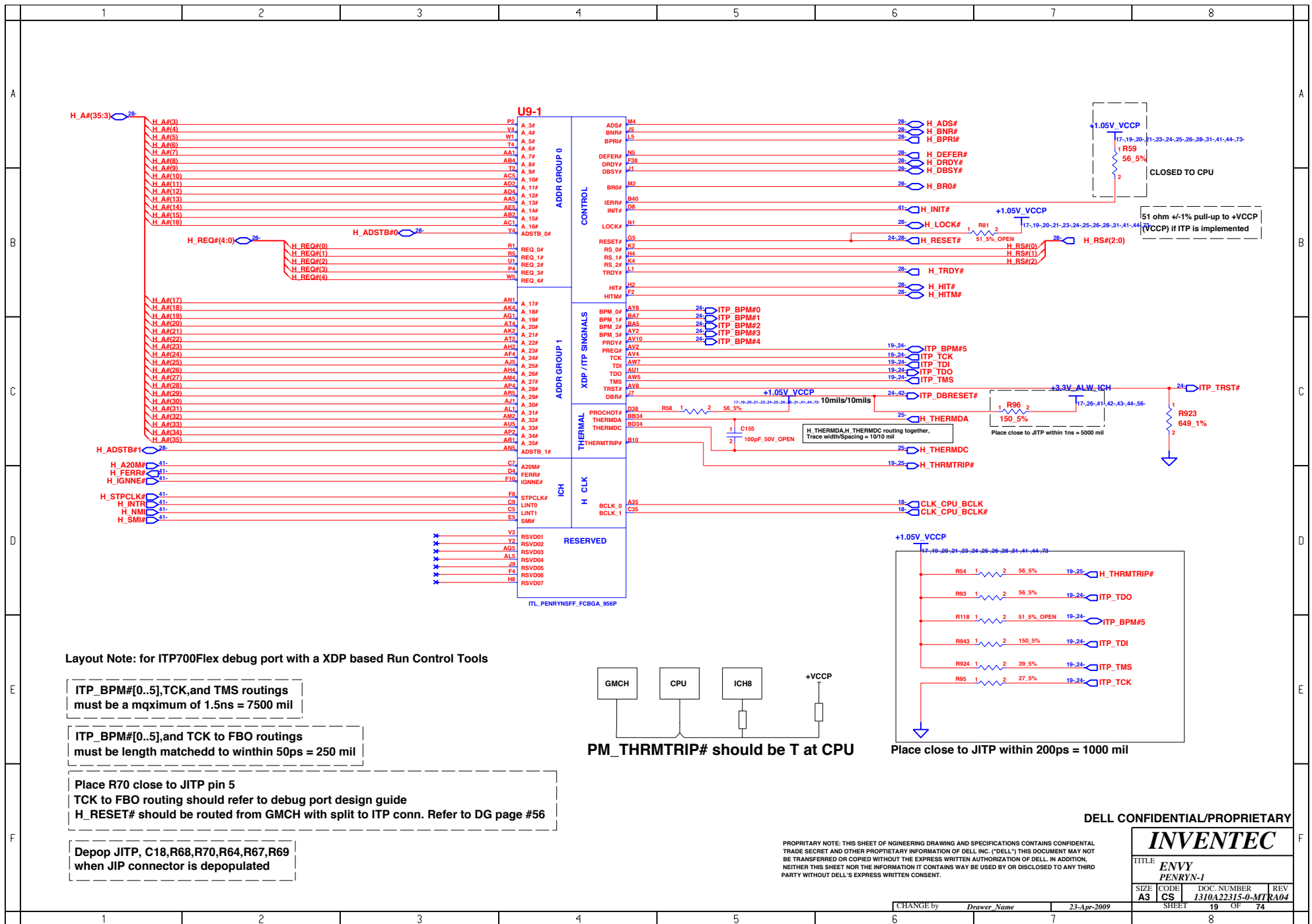
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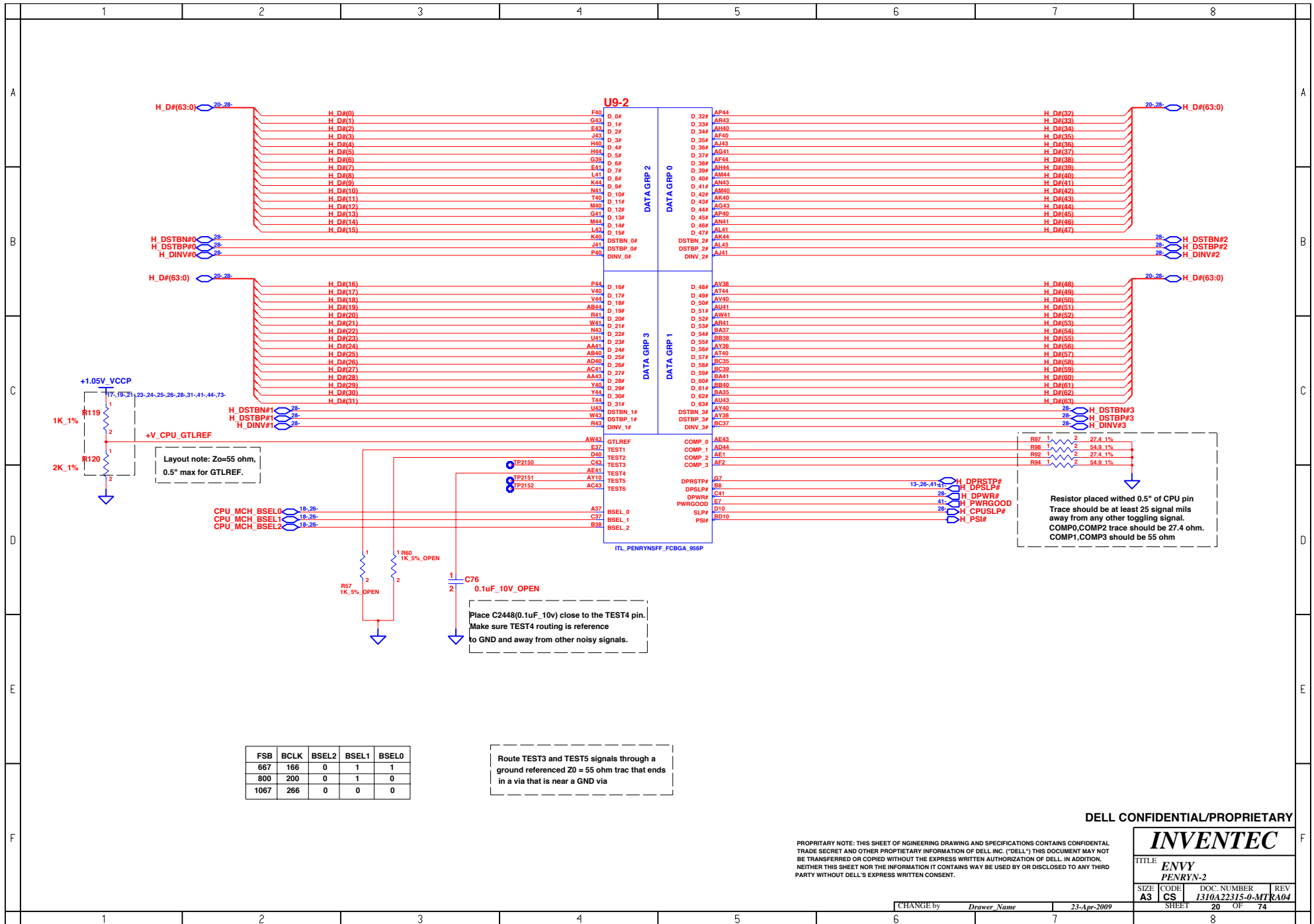
CHANGE by: Drawer Name 23-Apr-2009











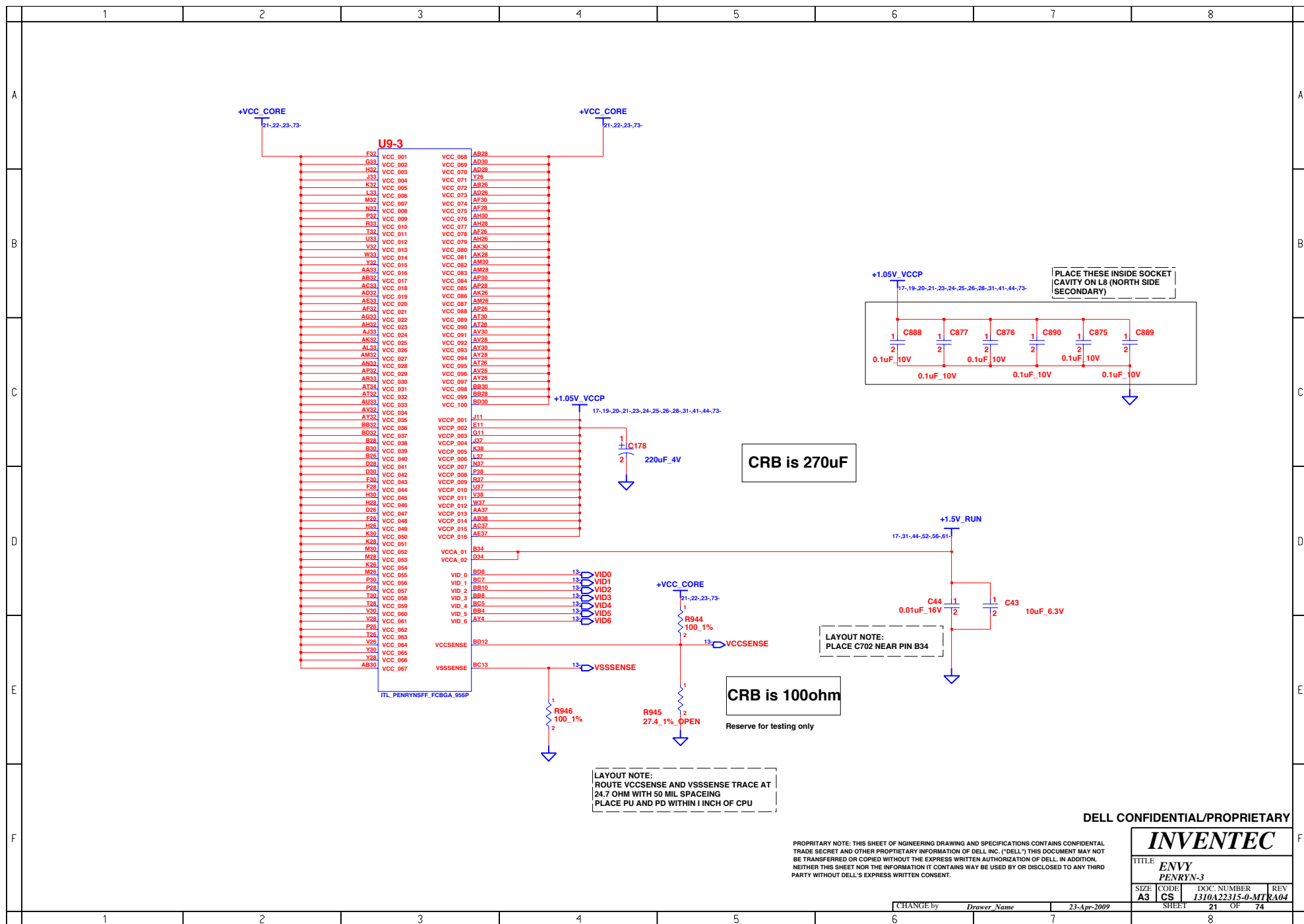
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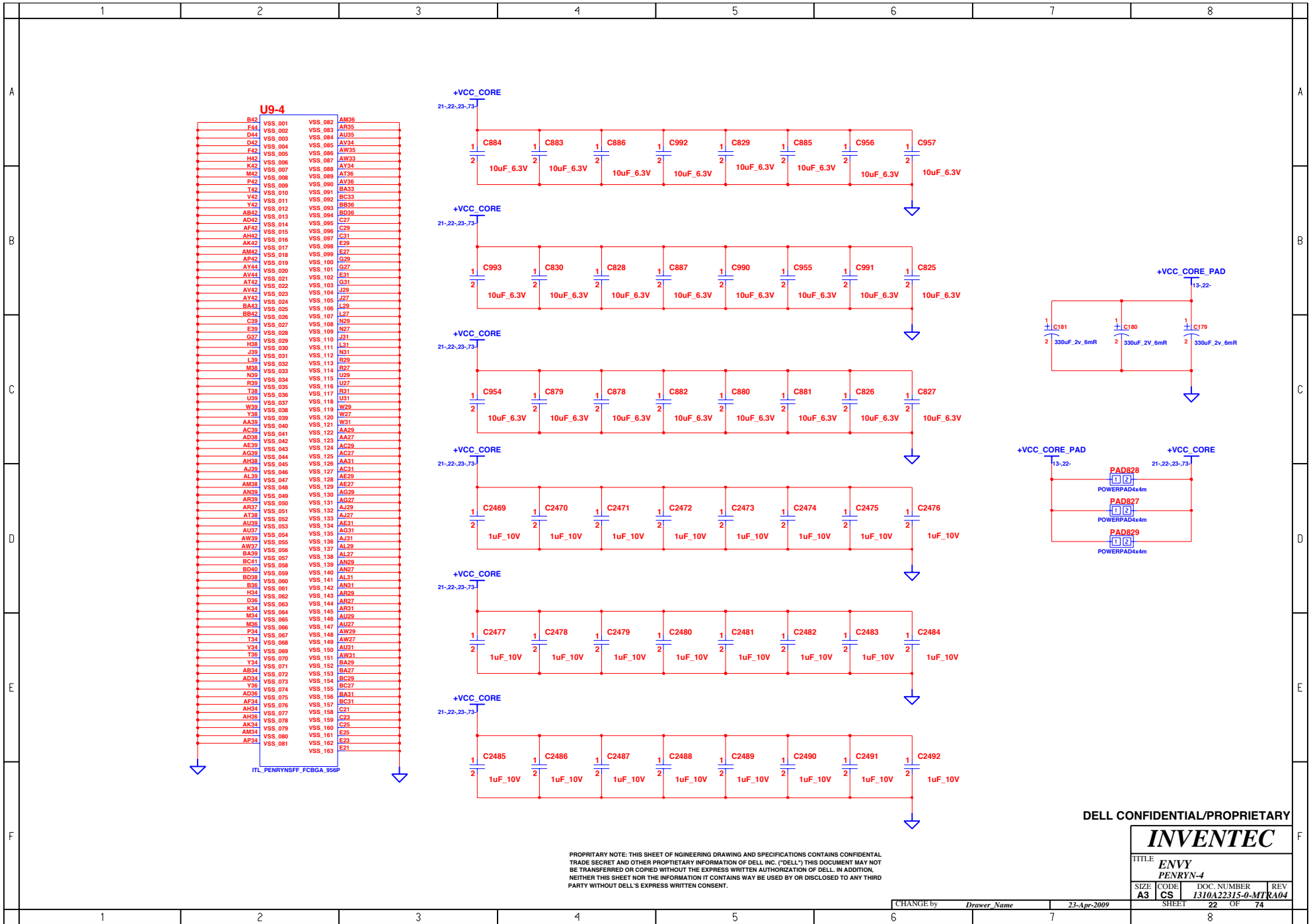
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TITLE ENVY PENRYN-2			
SIZE A3	CODE CS	DOC. NUMBER 1310A22315-0-MTR	REV A04
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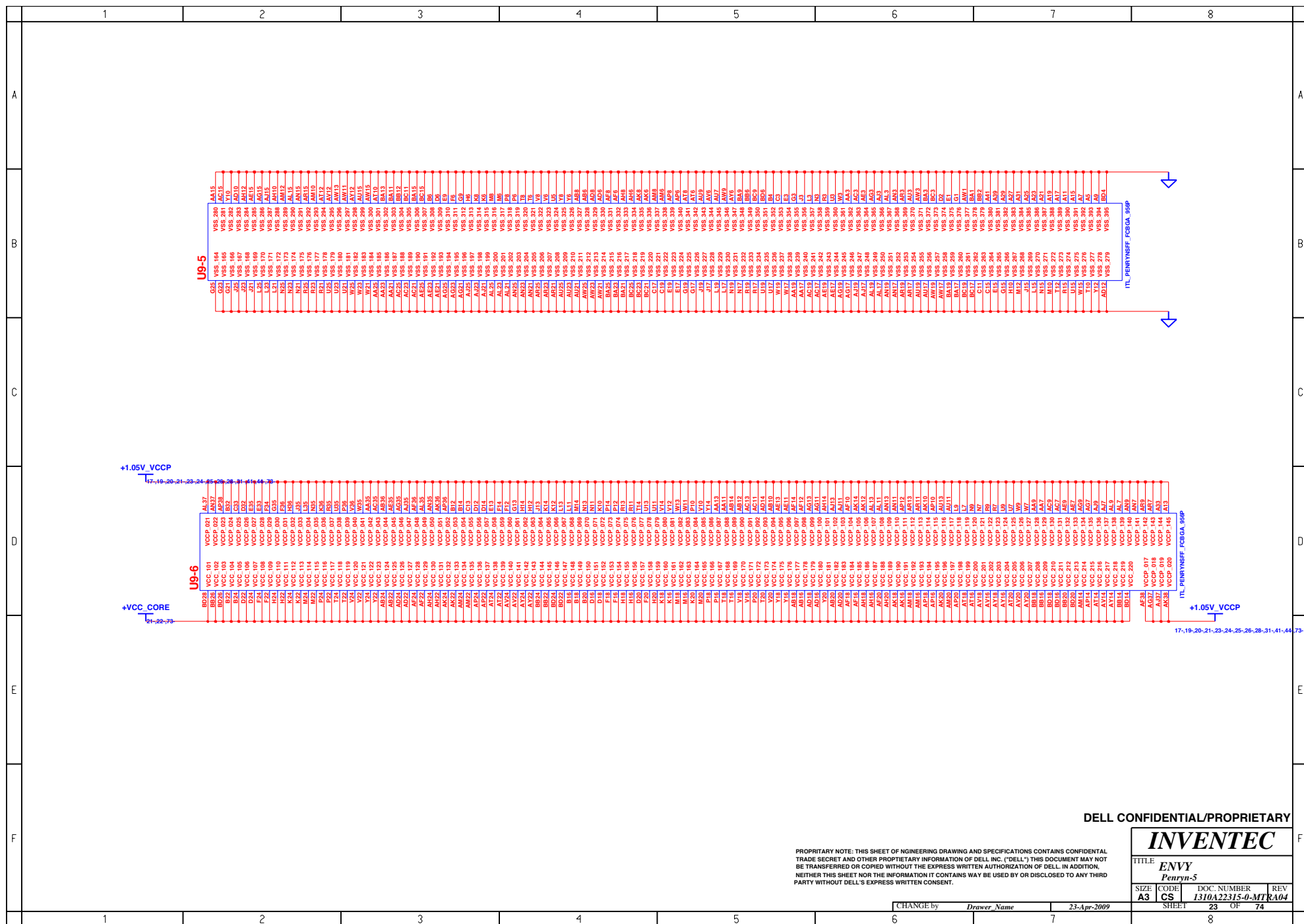
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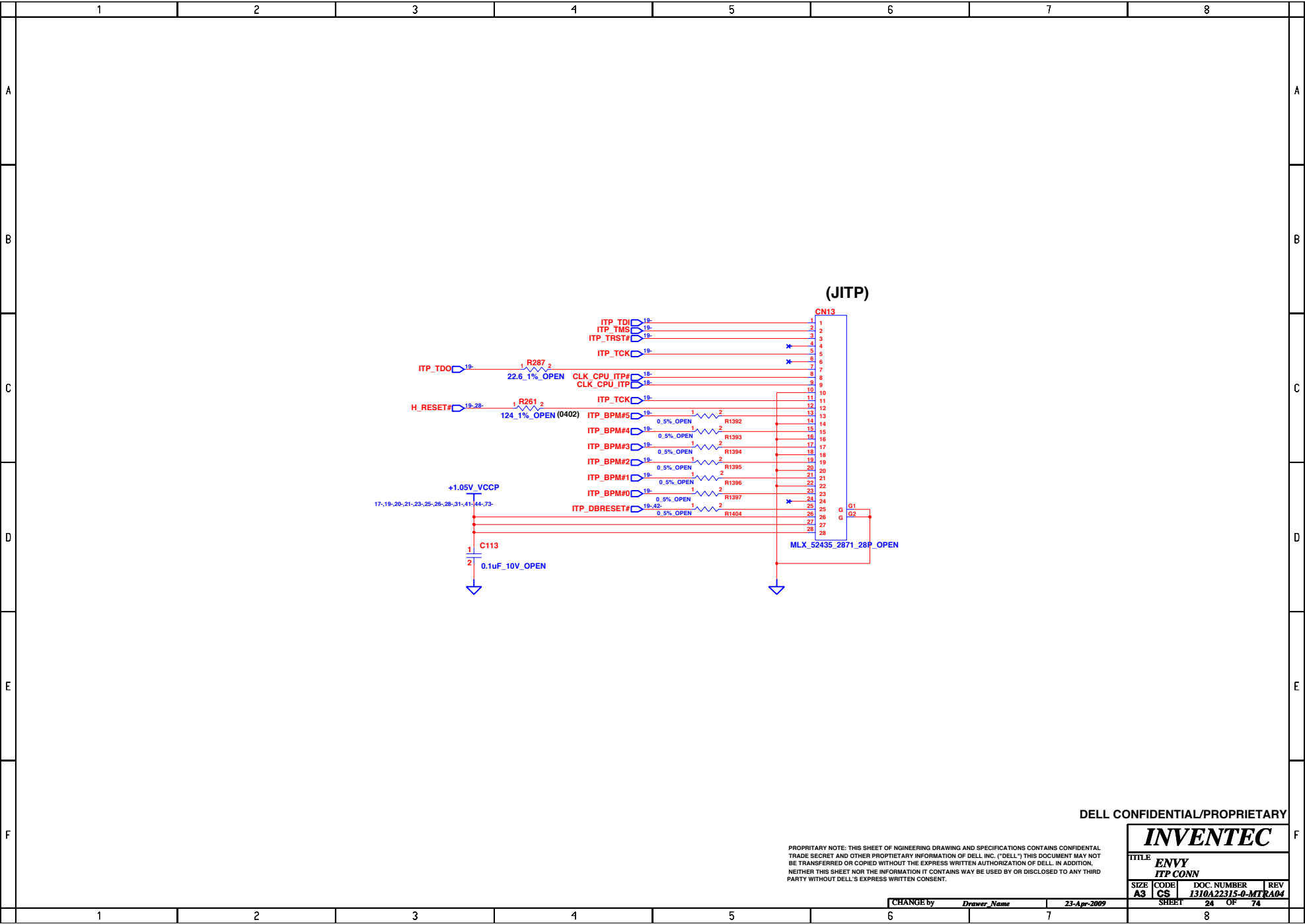
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TITLE ENVY PENRYN-4			
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SHEET 22		OF 74	

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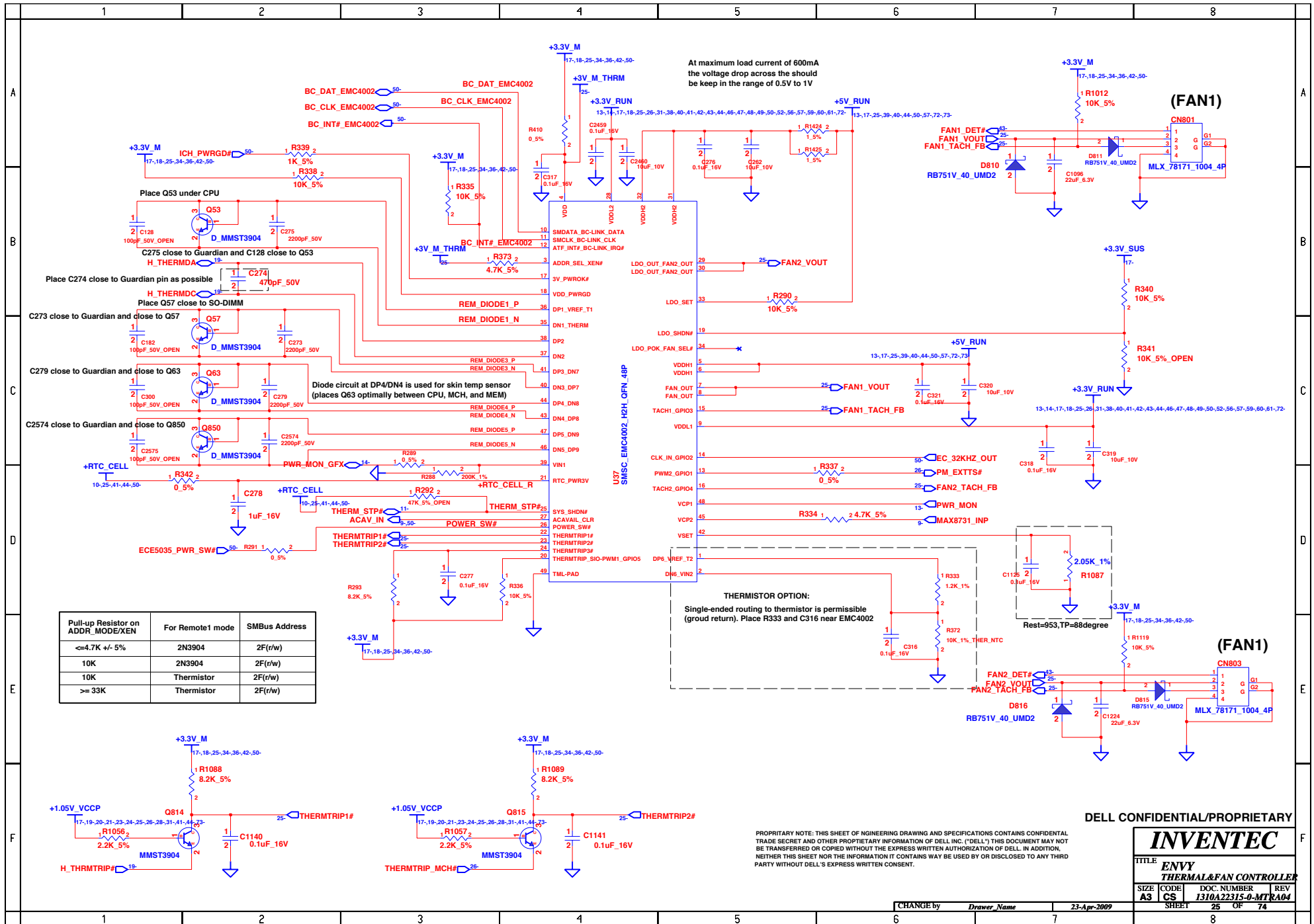
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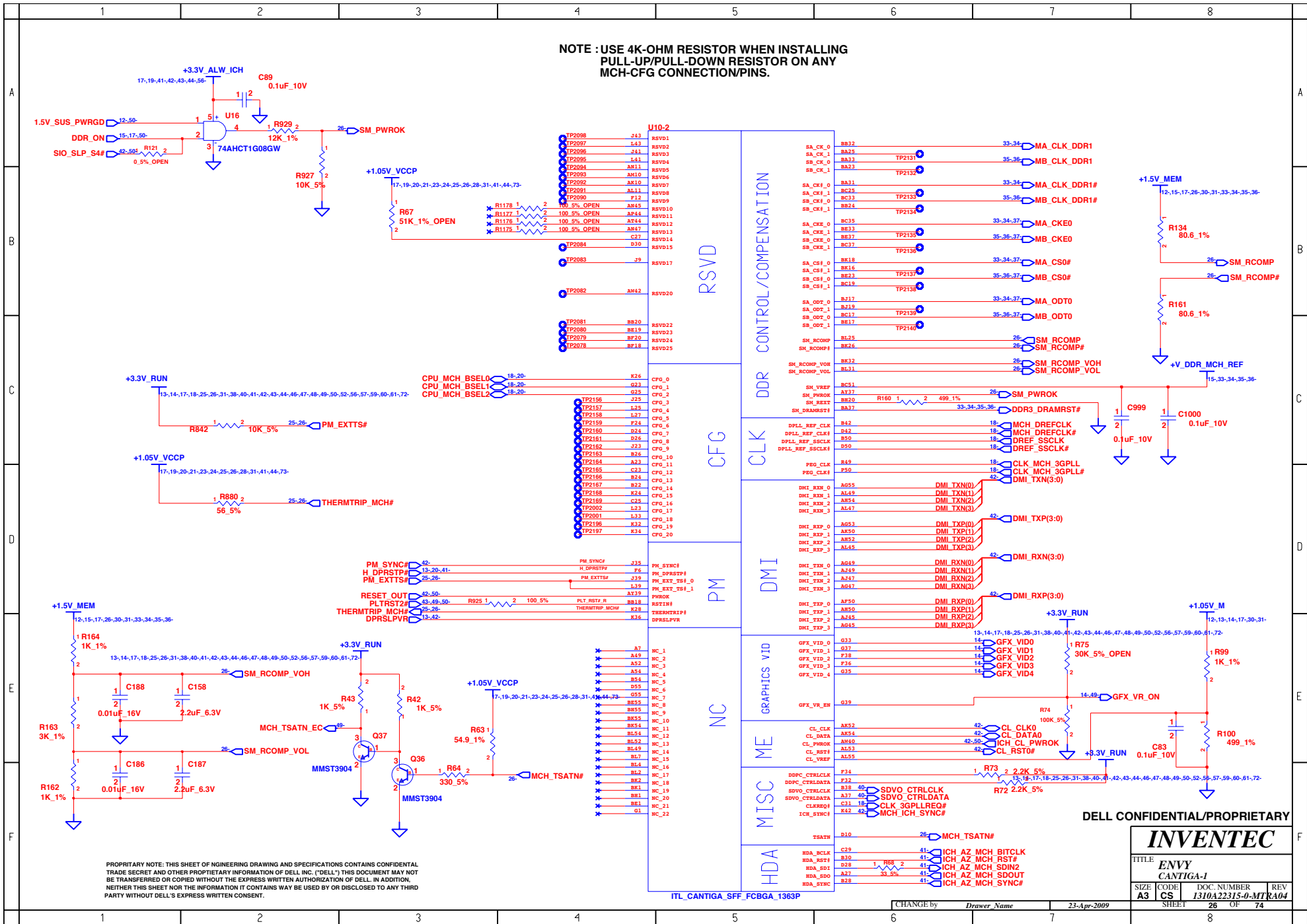
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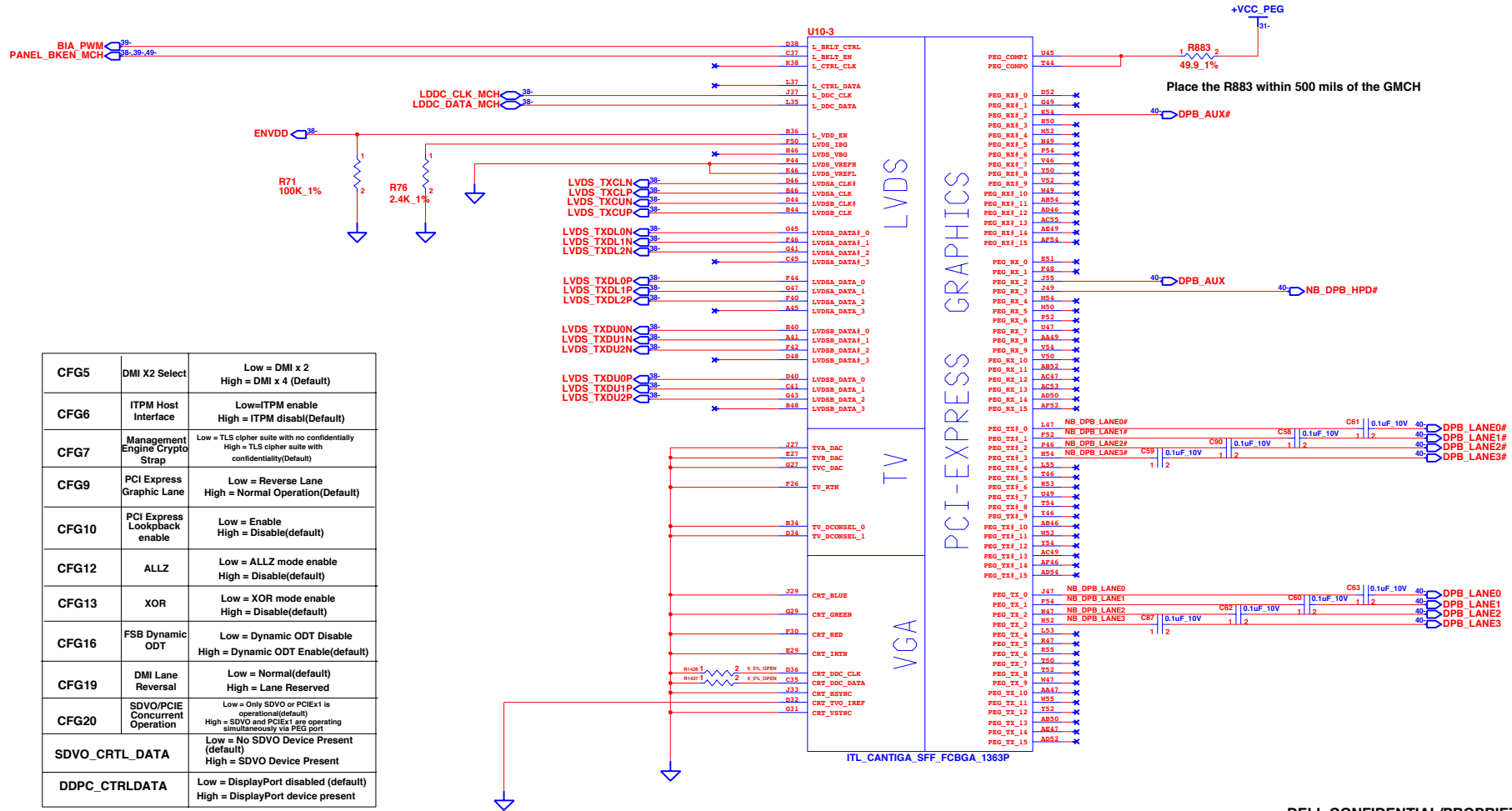
SIZE A3	CODE CS	DOC. NUMBER 1310A22315-0-MTRA04	REV
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CHANGE by	<i>Drawer Name</i>	23-Apr-2009
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Q

CFG5	DMI X2 Select	Low = DMI x 2 High = DMI x 4 (Default)
CFG6	ITPM Host Interface	Low=ITPM enable High = ITPM disabl(Default)
CFG7	Management Engine Crypto Strap	Low = TLS cipher suite with no confidentiality High = TLS cipher suite with confidentiality(Default)
CFG9	PCI Express Graphic Lane	Low = Reverse Lane High = Normal Operation(Default)
CFG10	PCI Express Lookpback enable	Low = Enable High = Disable(default)
CFG12	ALLZ	Low = ALLZ mode enable High = Disable(default)
CFG13	XOR	Low = XOR mode enable High = Disable(default)
CFG16	FSB Dynamic ODT	Low = Dynamic ODT Disable High = Dynamic ODT Enable(default)
CFG19	DMI Lane Reversal	Low = Normal(default) High = Lane Reserved
CFG20	SDVO/PCIE Concurrent Operation	Low = Only SDVO or PCIe1 is operational(default) High = SDVO and PCIe1 are operating simultaneously via PEG port
SDVO_CTRL_DATA		Low = No SDVO Device Present (default) High = SDVO Device Present
DDPC_CTRLDATA		Low = DisplayPort disabled (default) High = DisplayPort device present

CFG[5:16] have internal pullup CFG[19:20] have internal pulldown



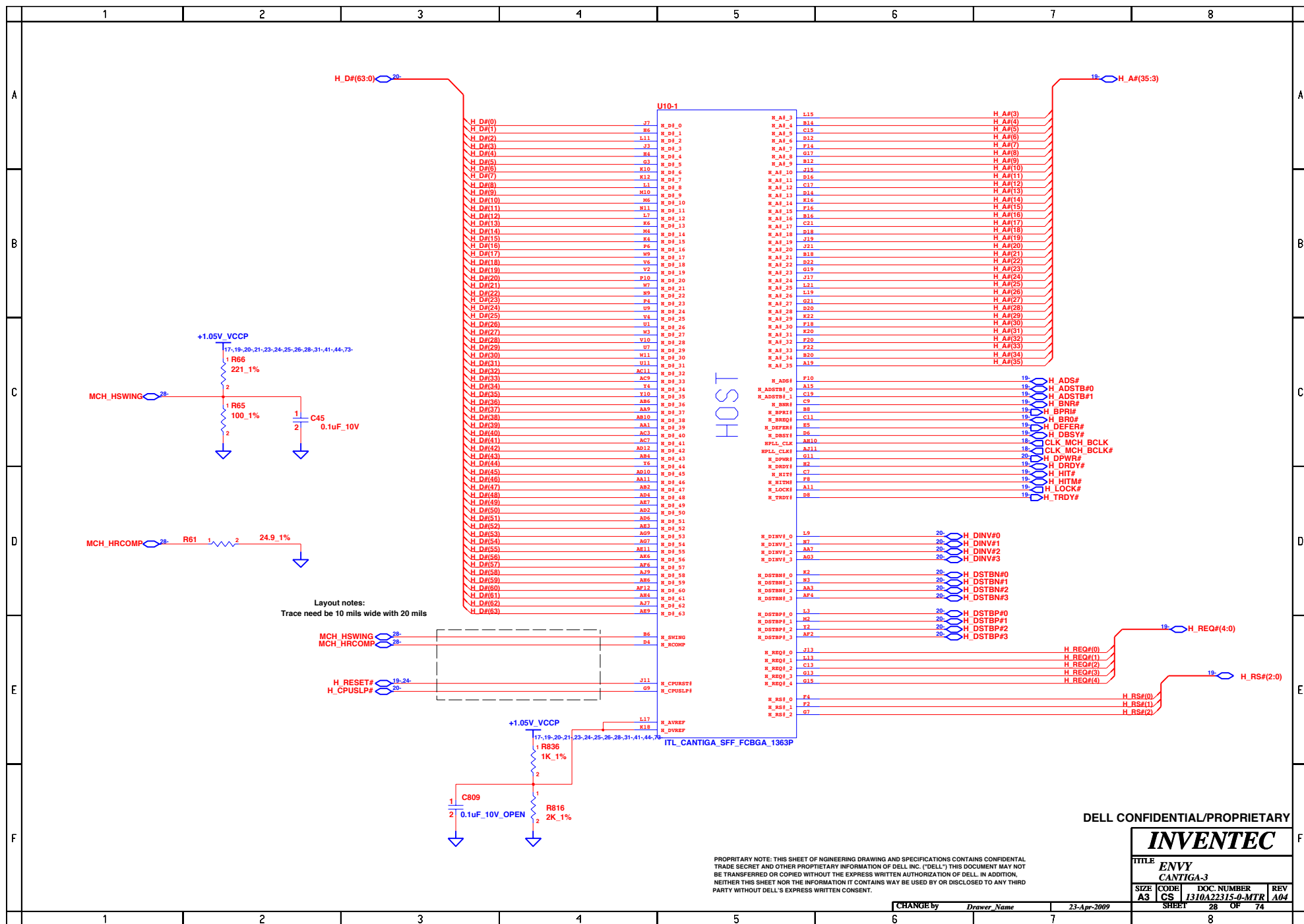
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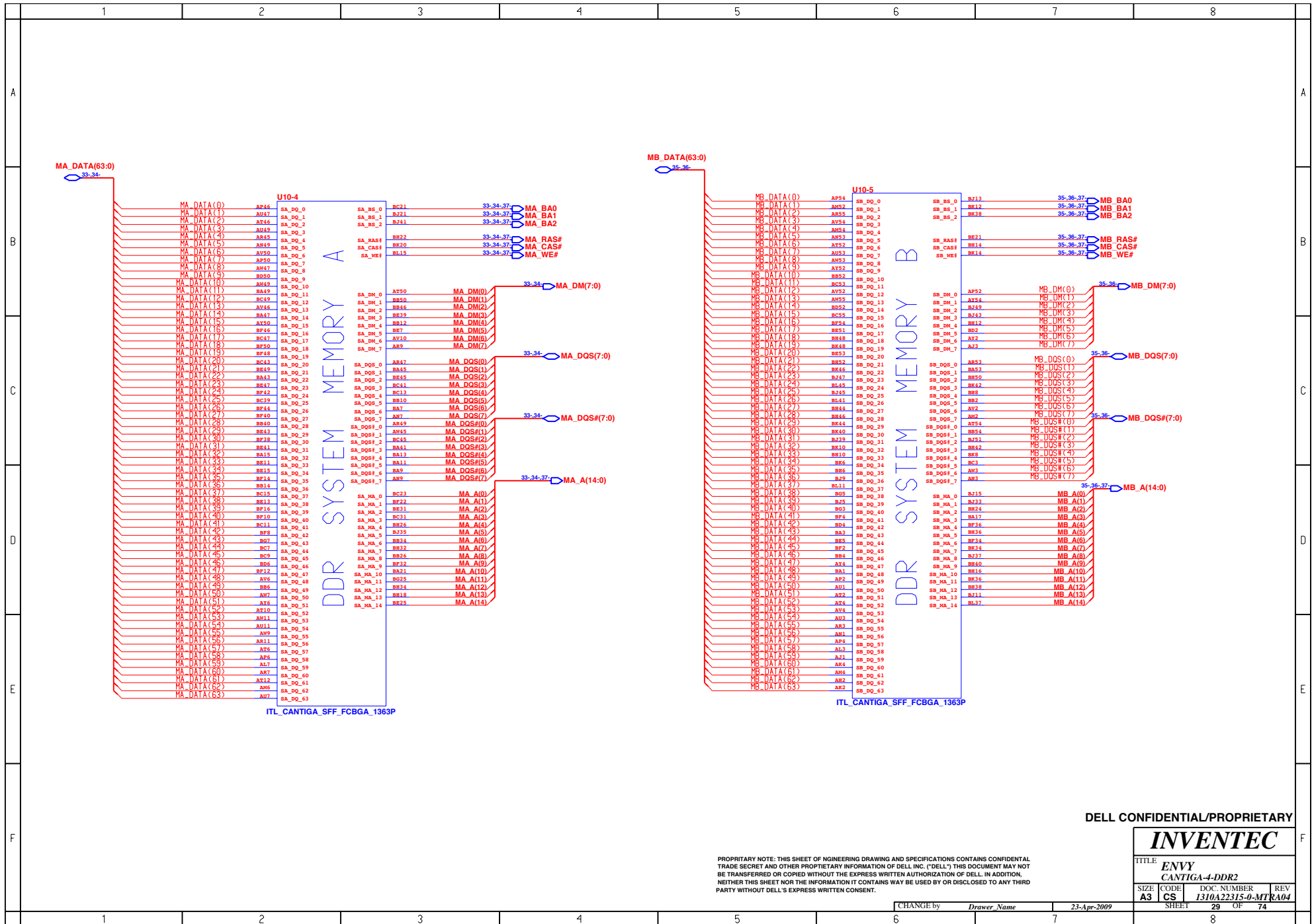
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TITLE ENVY CANTIGA-2			
SIZE A3	CODE CS	DOC. NUMBER I310A22315-0-MTR	REV A04
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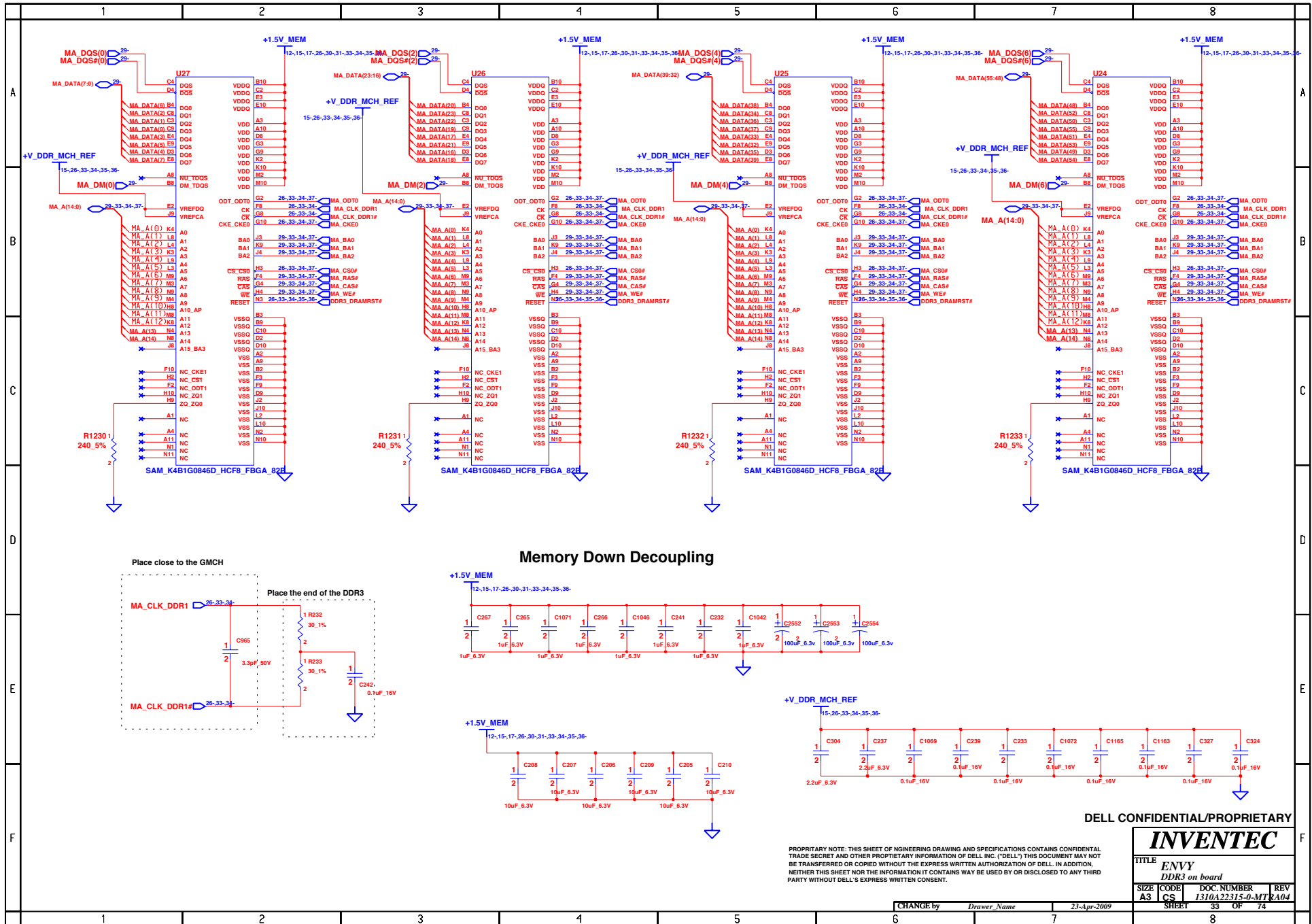
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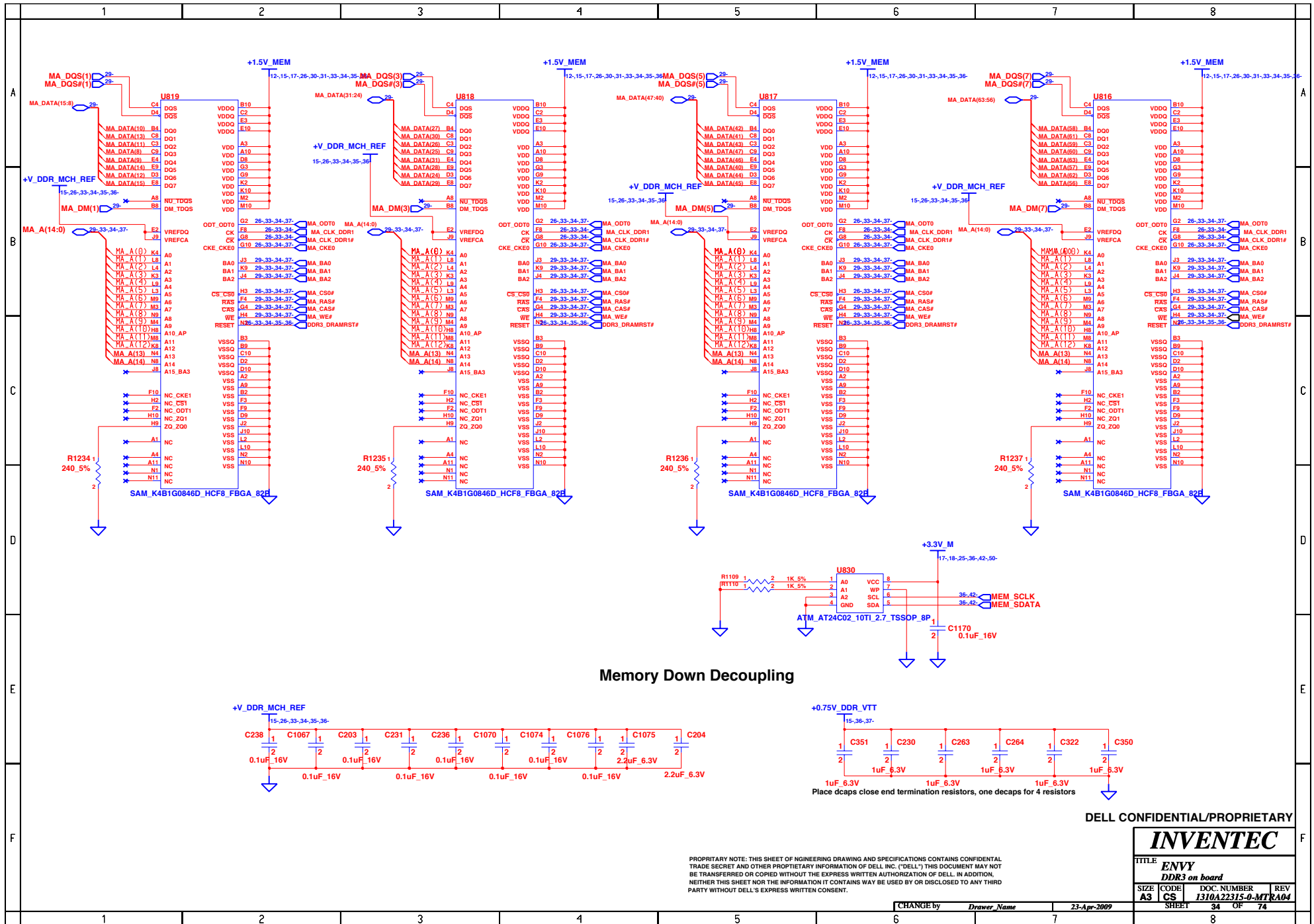
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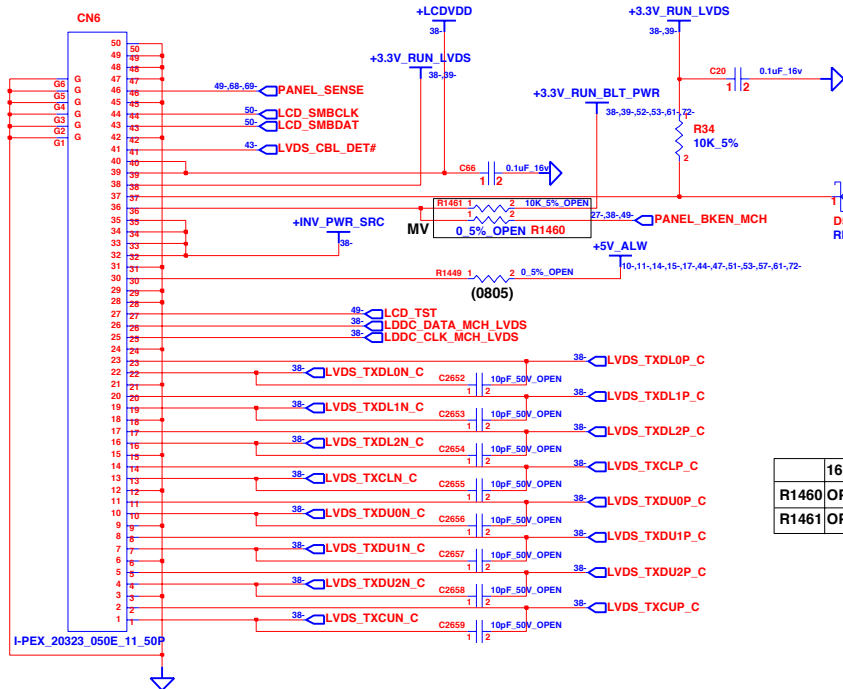
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TITLE: ENVY			
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SIZE	CODE	DOC. NUMBER	REV
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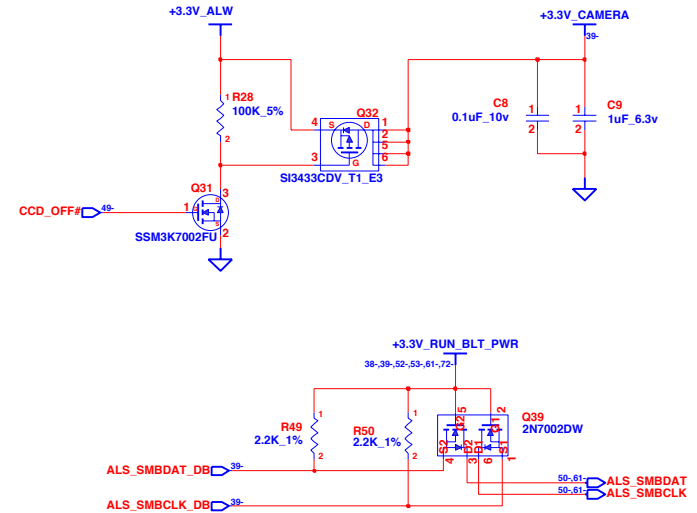
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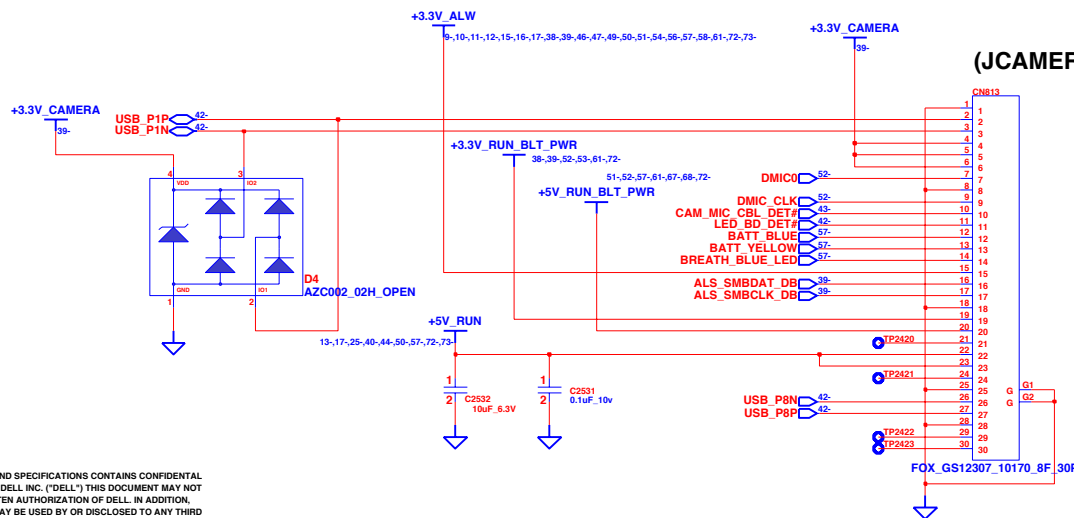


	16" PANEL	15" PANEL
R1460	OPEN	0.5% .0402
R1461	OPEN	10K 5% .0402

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(JCAMERA)



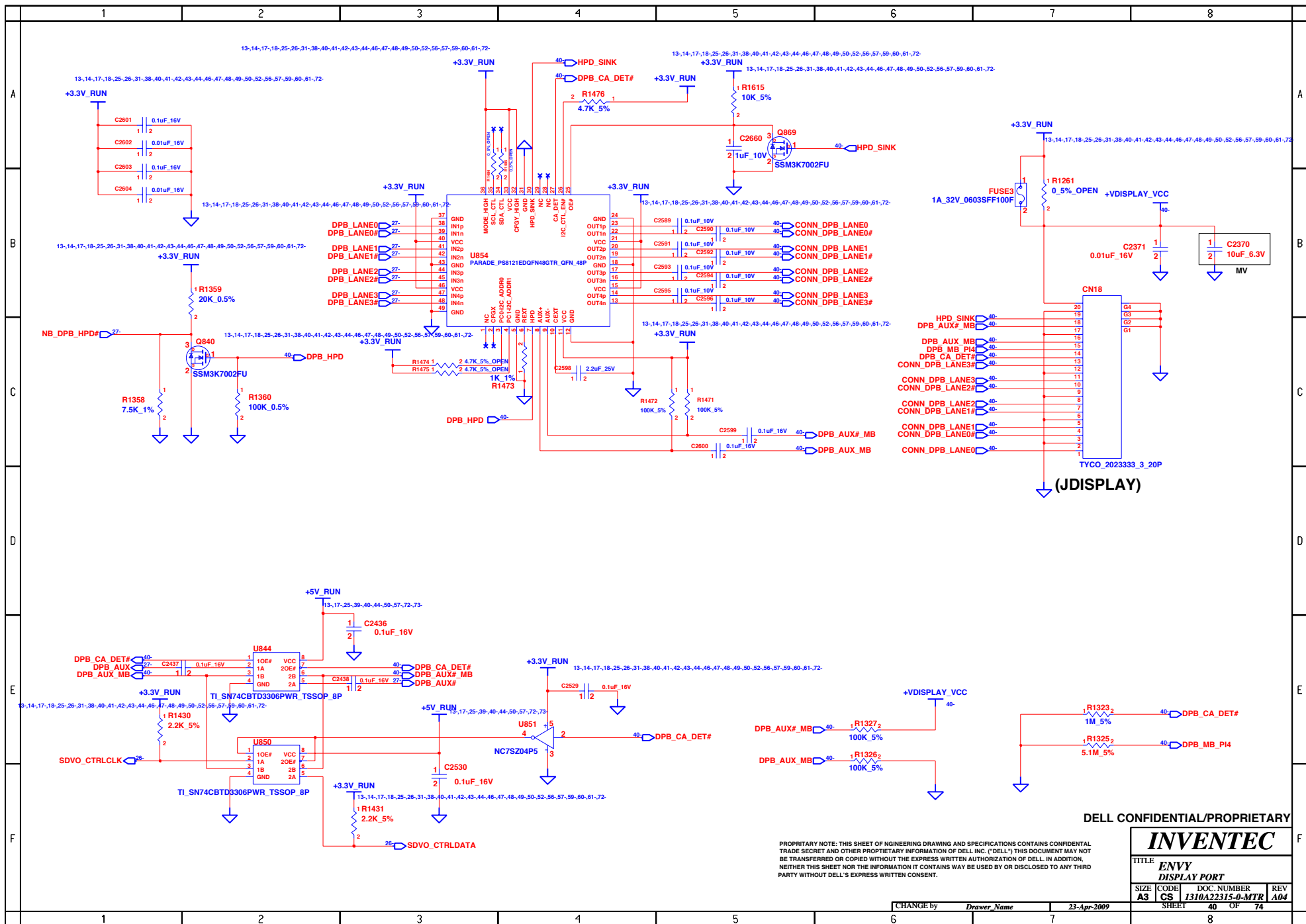
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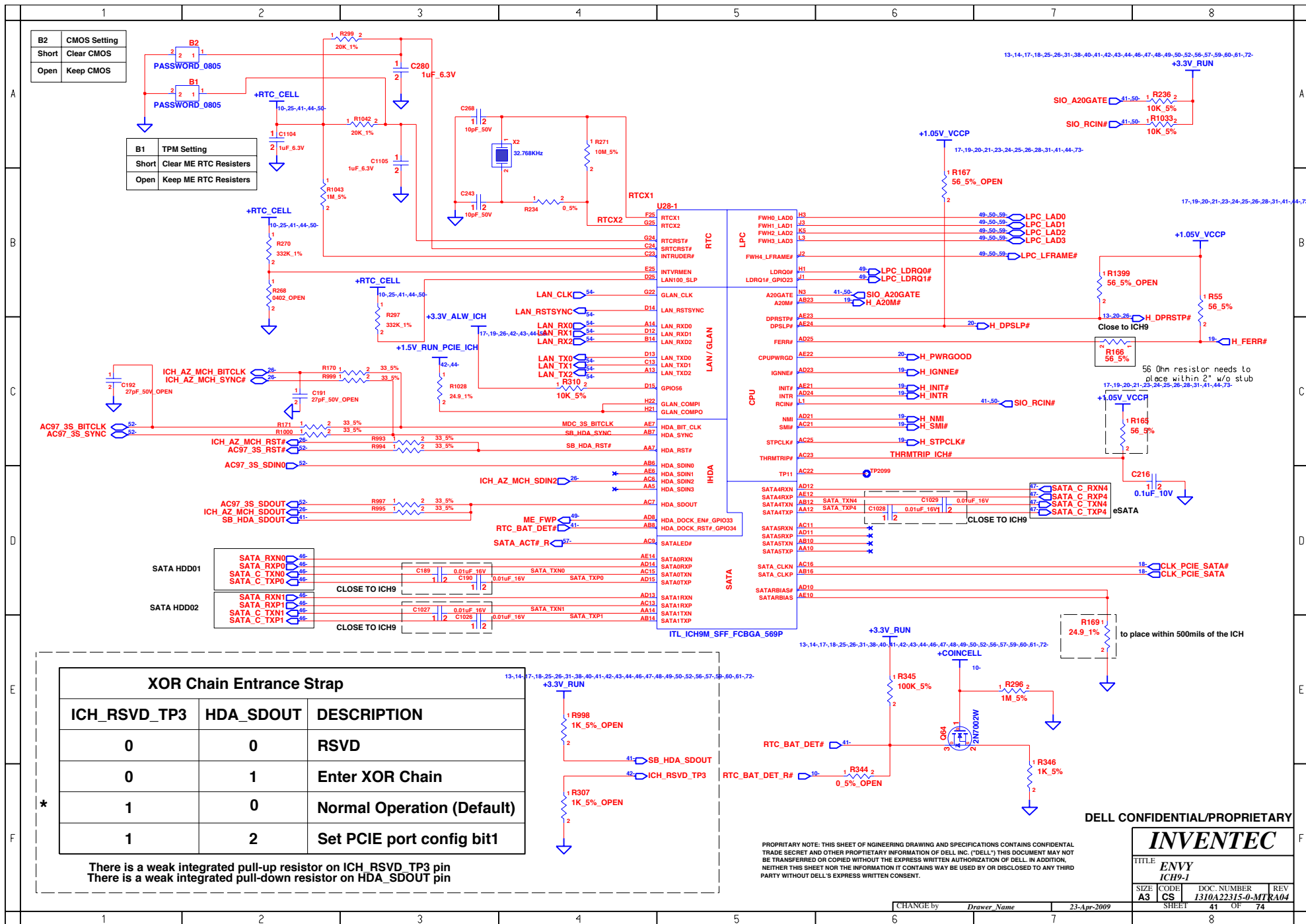
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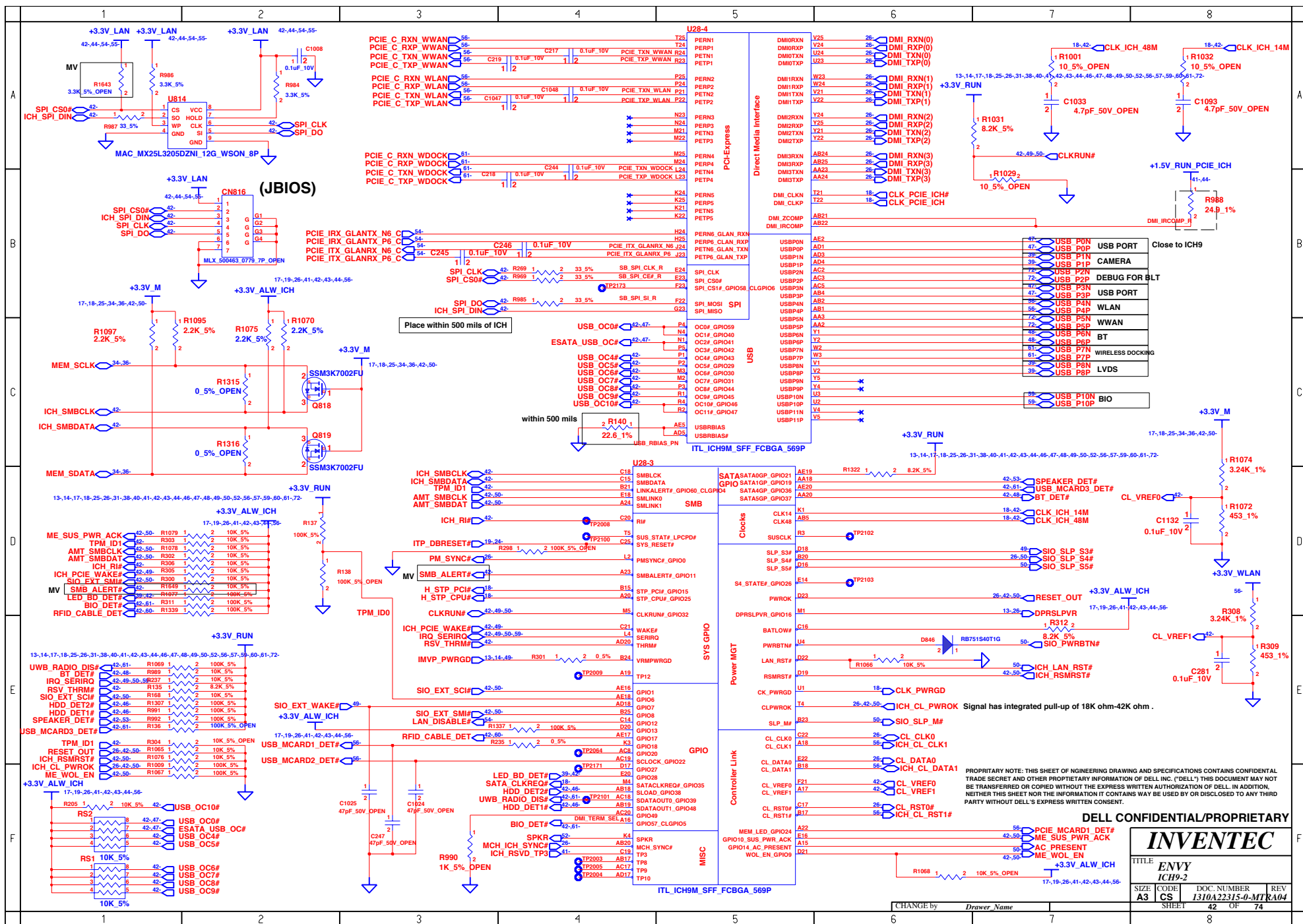
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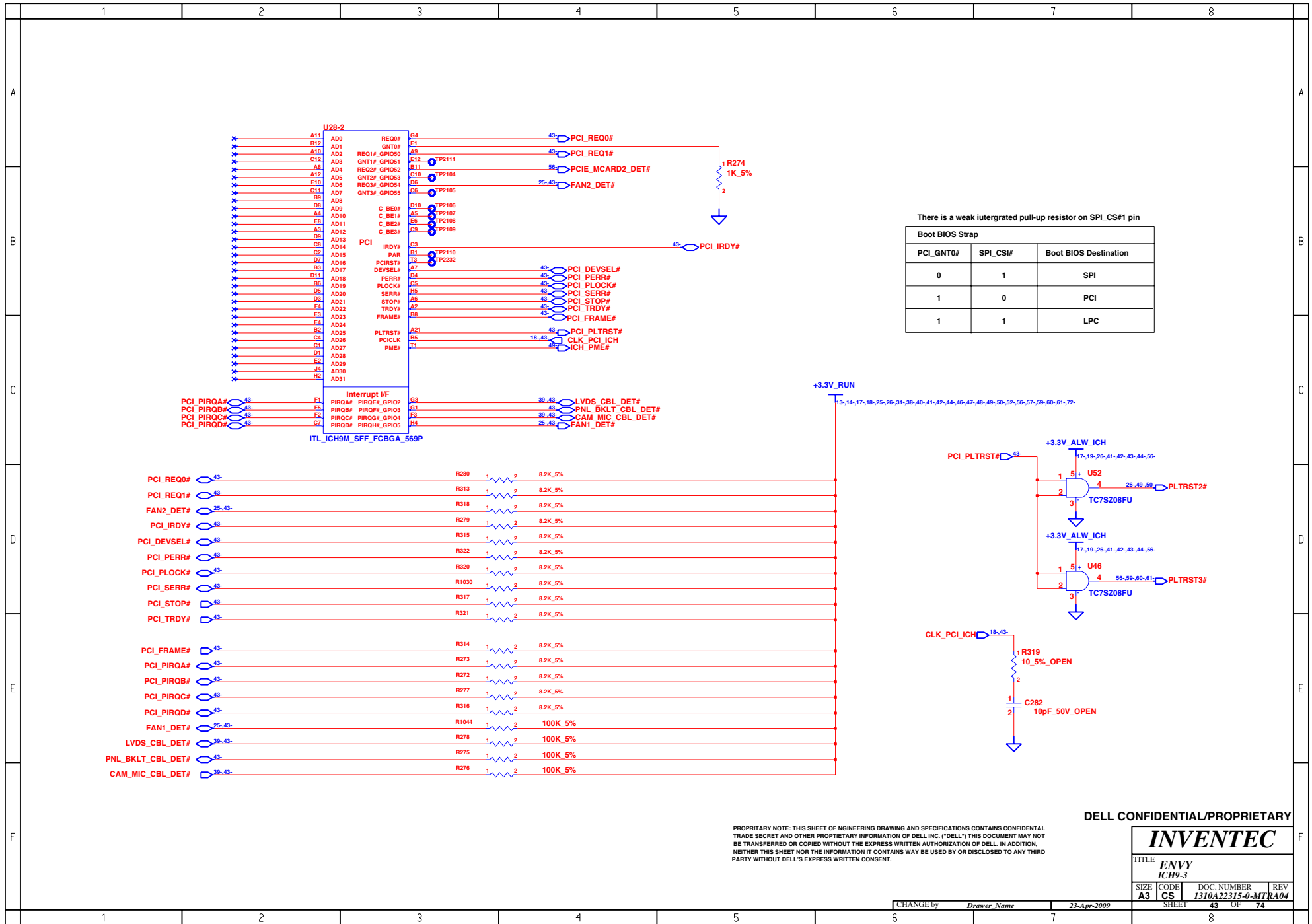
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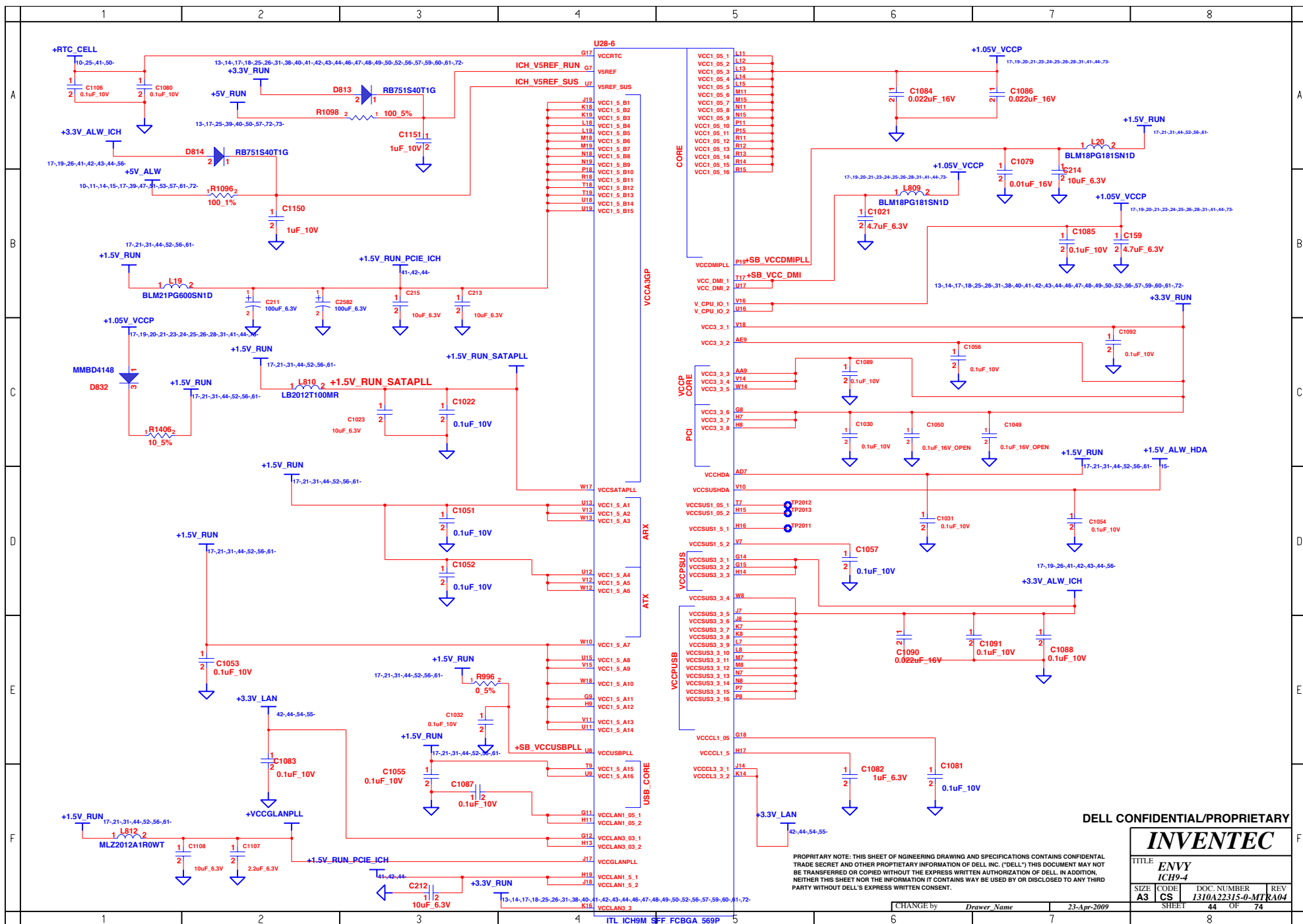


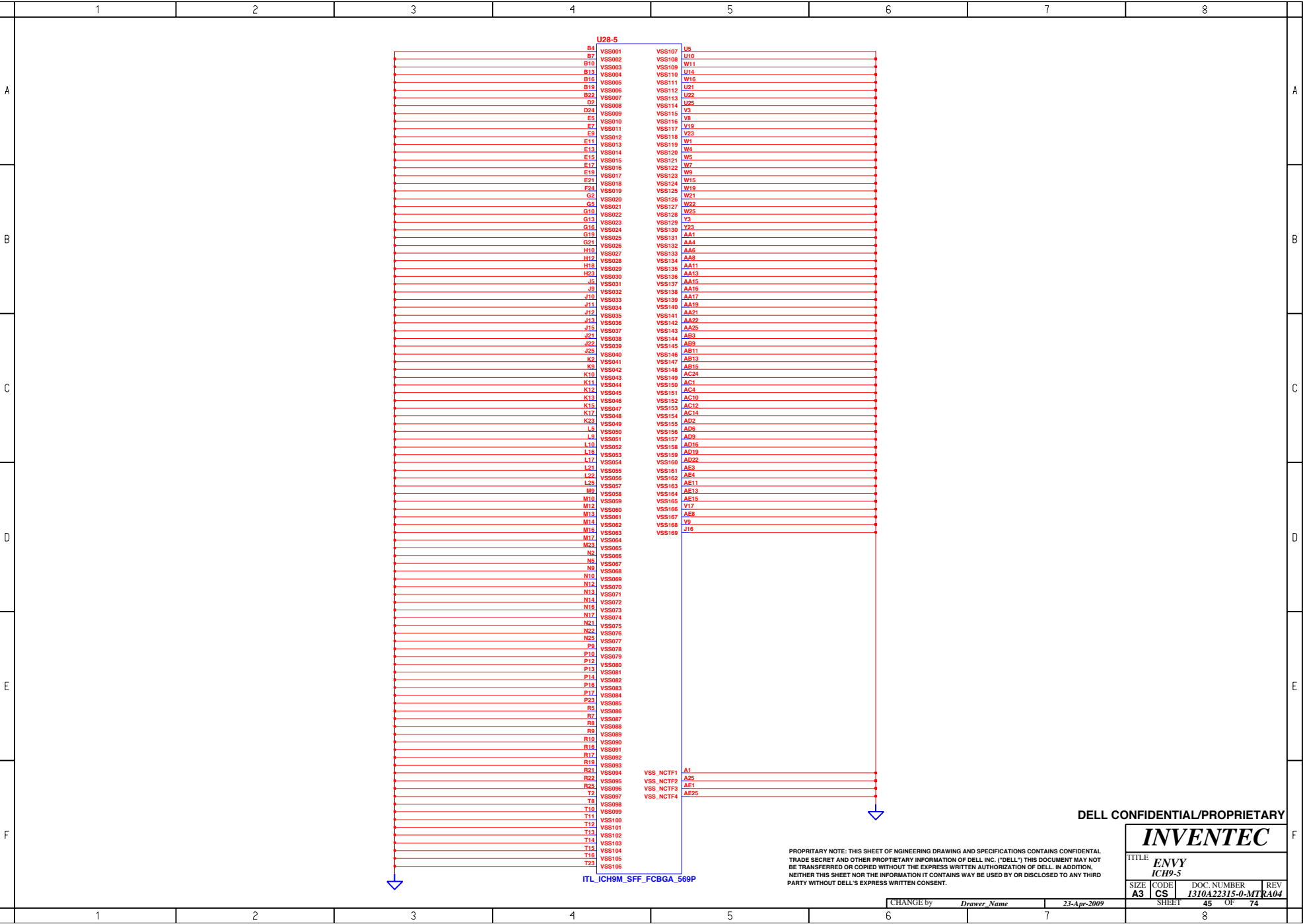
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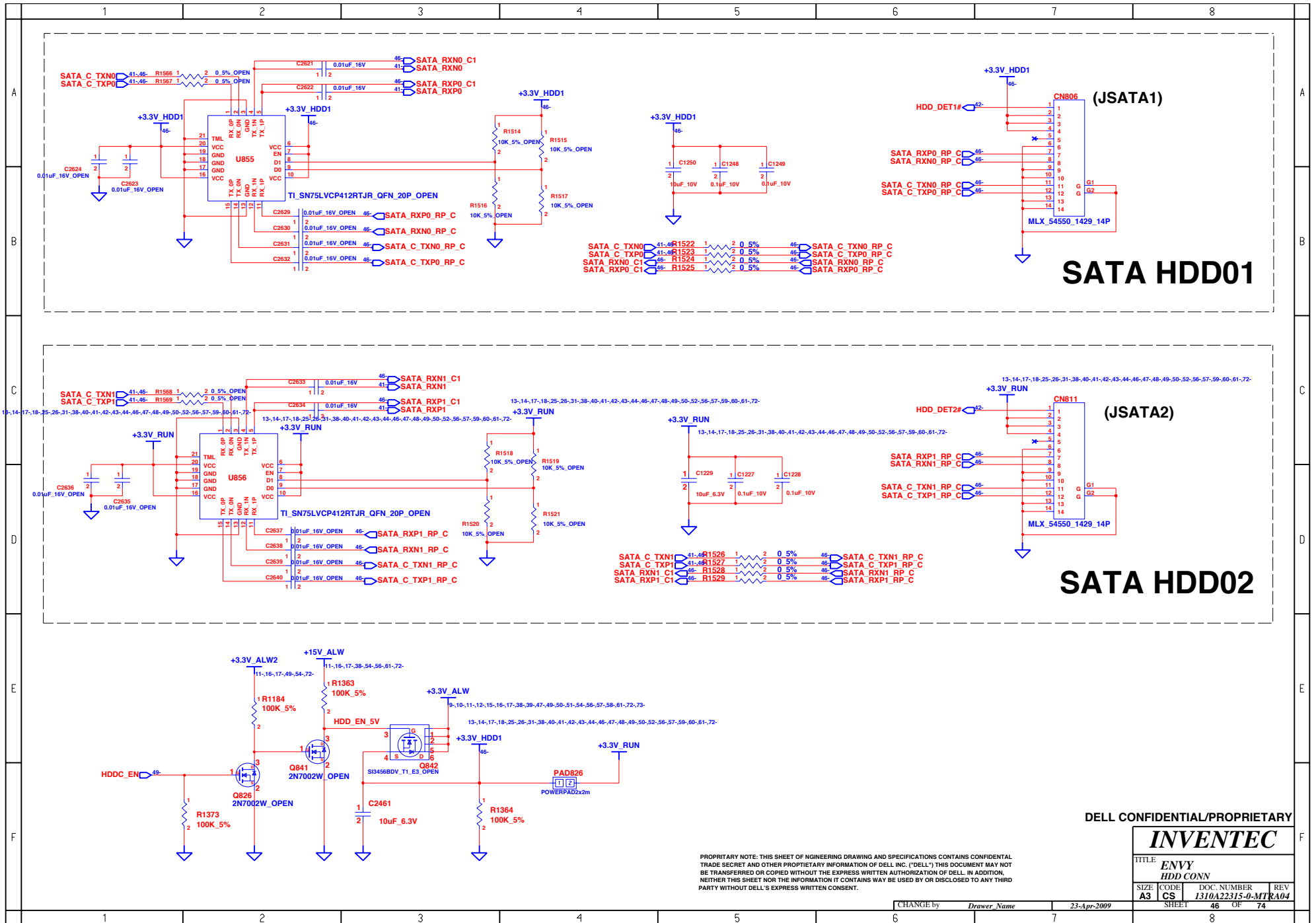


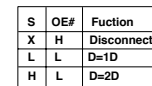
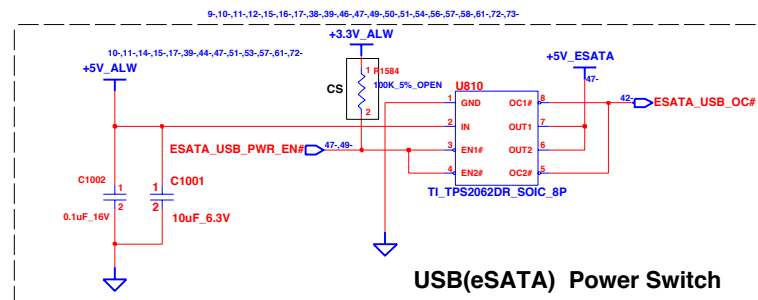






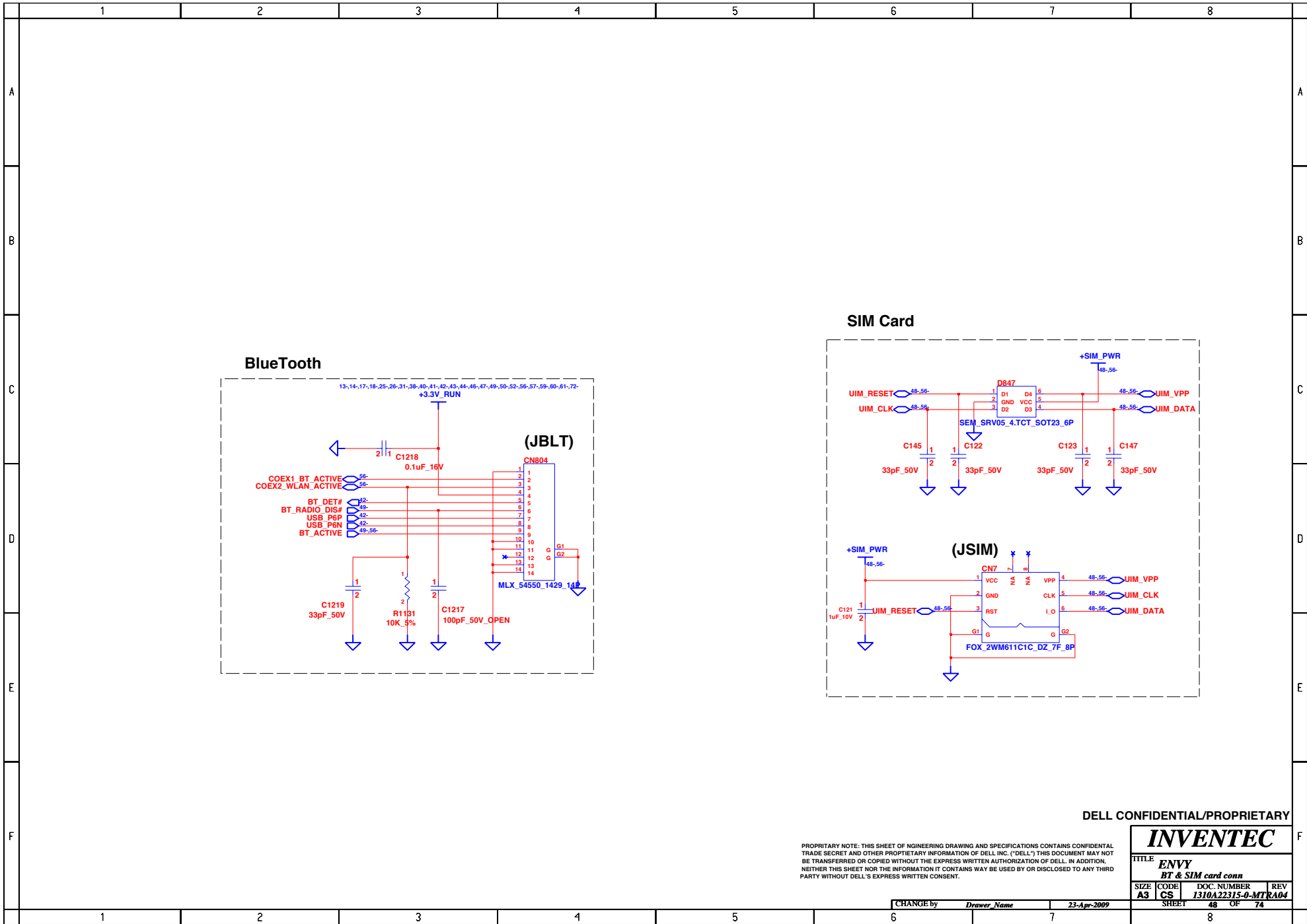


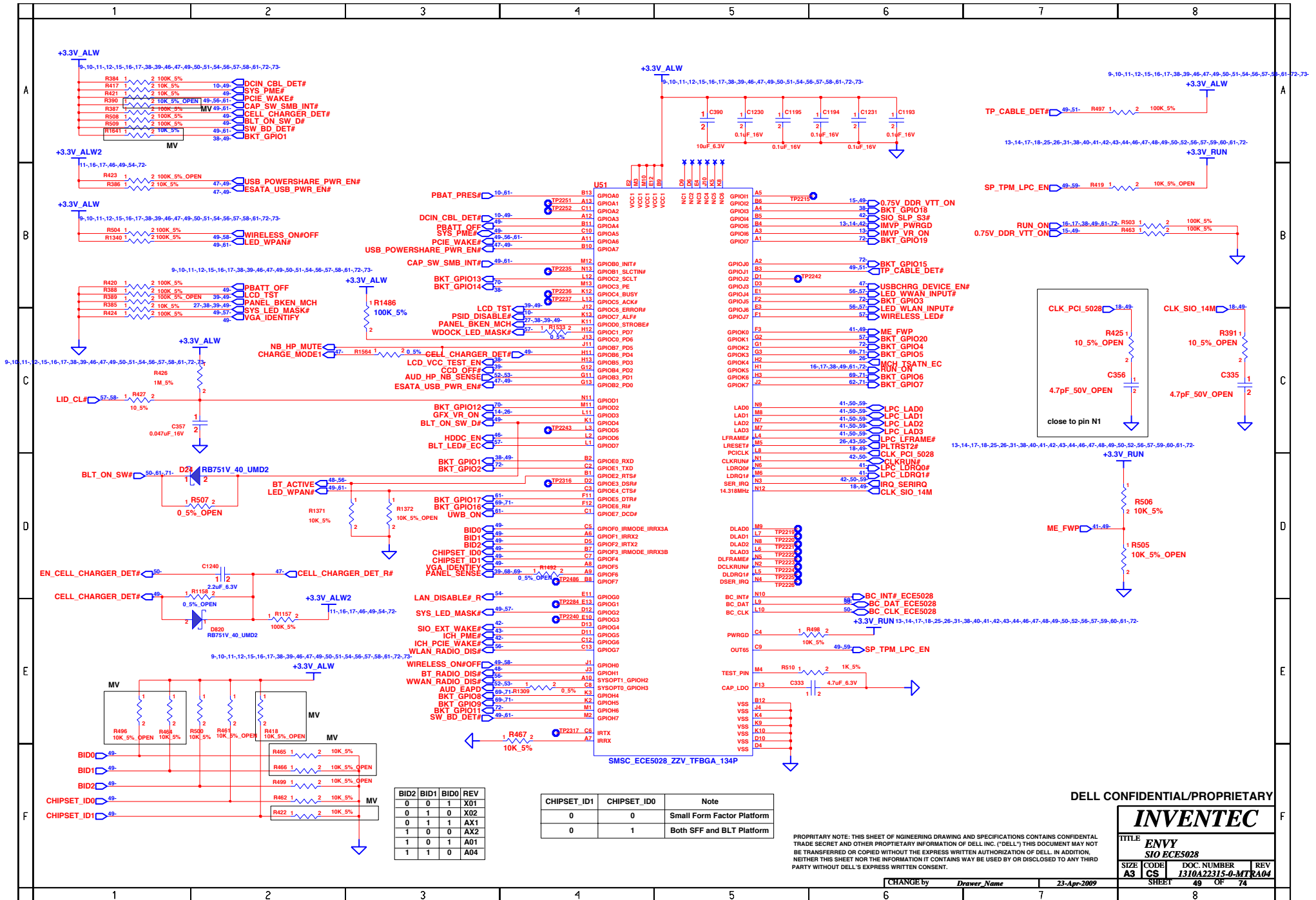


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ENVY USB/eSATA CONN			
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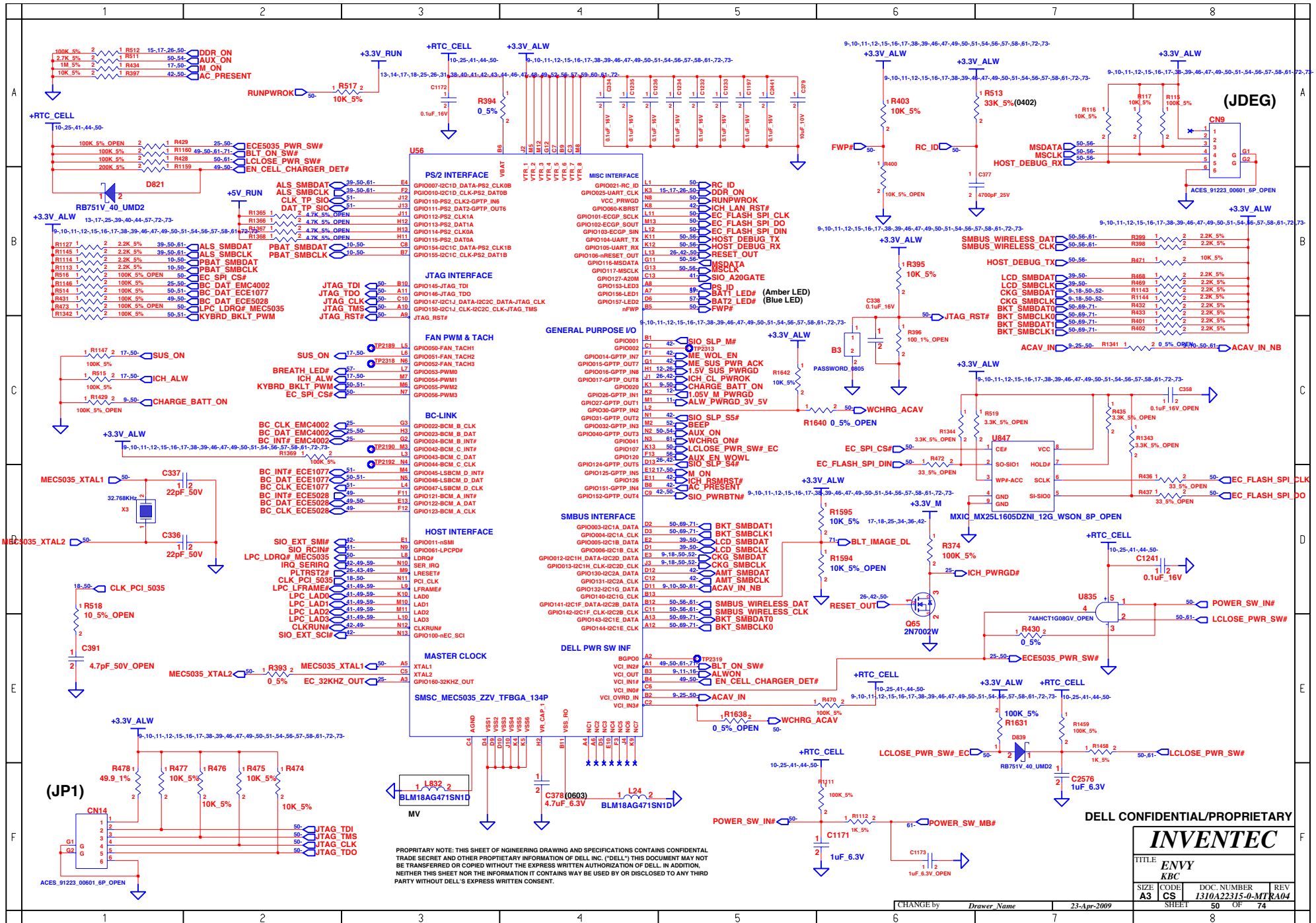
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SHEET 1		OF 74	

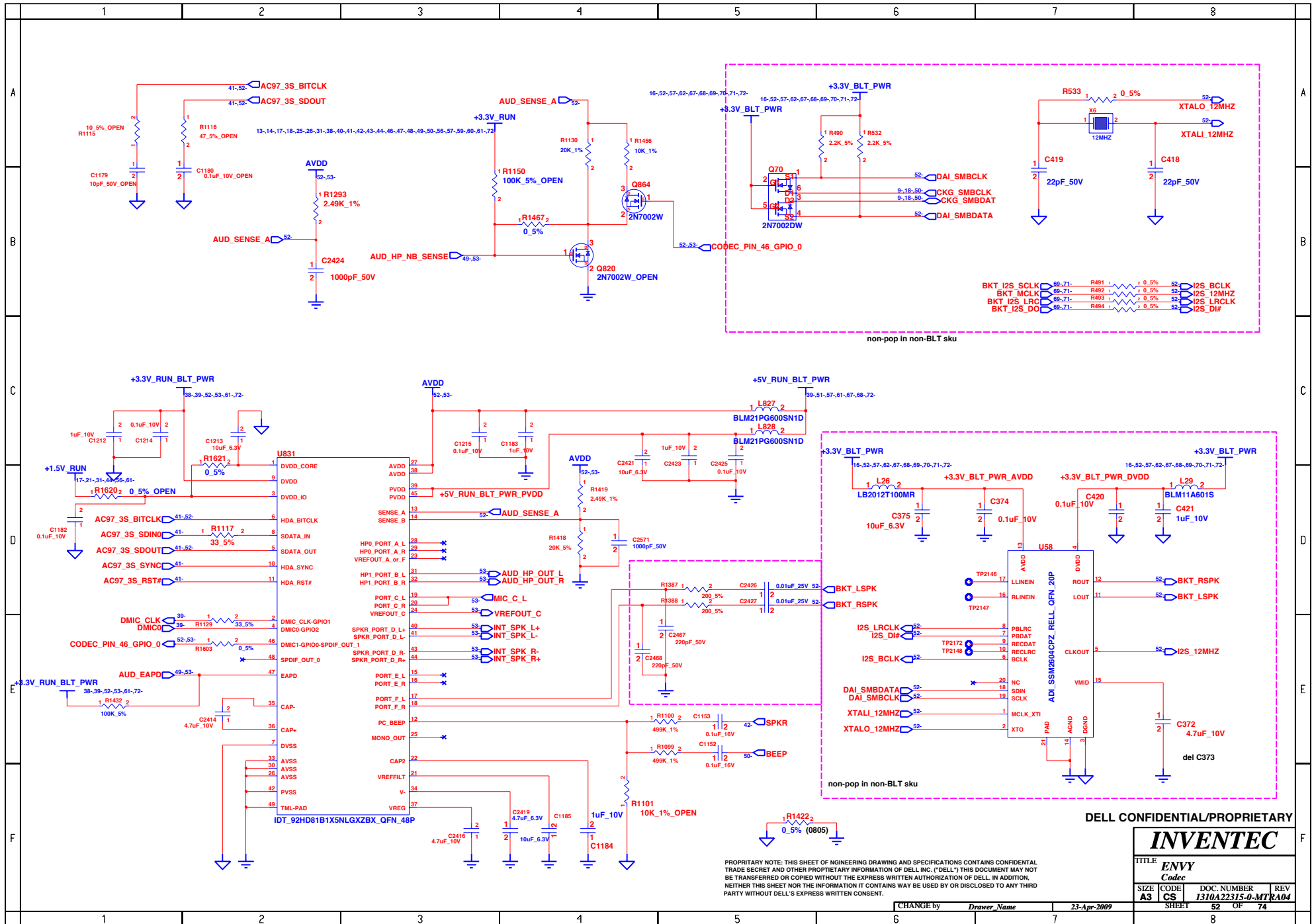
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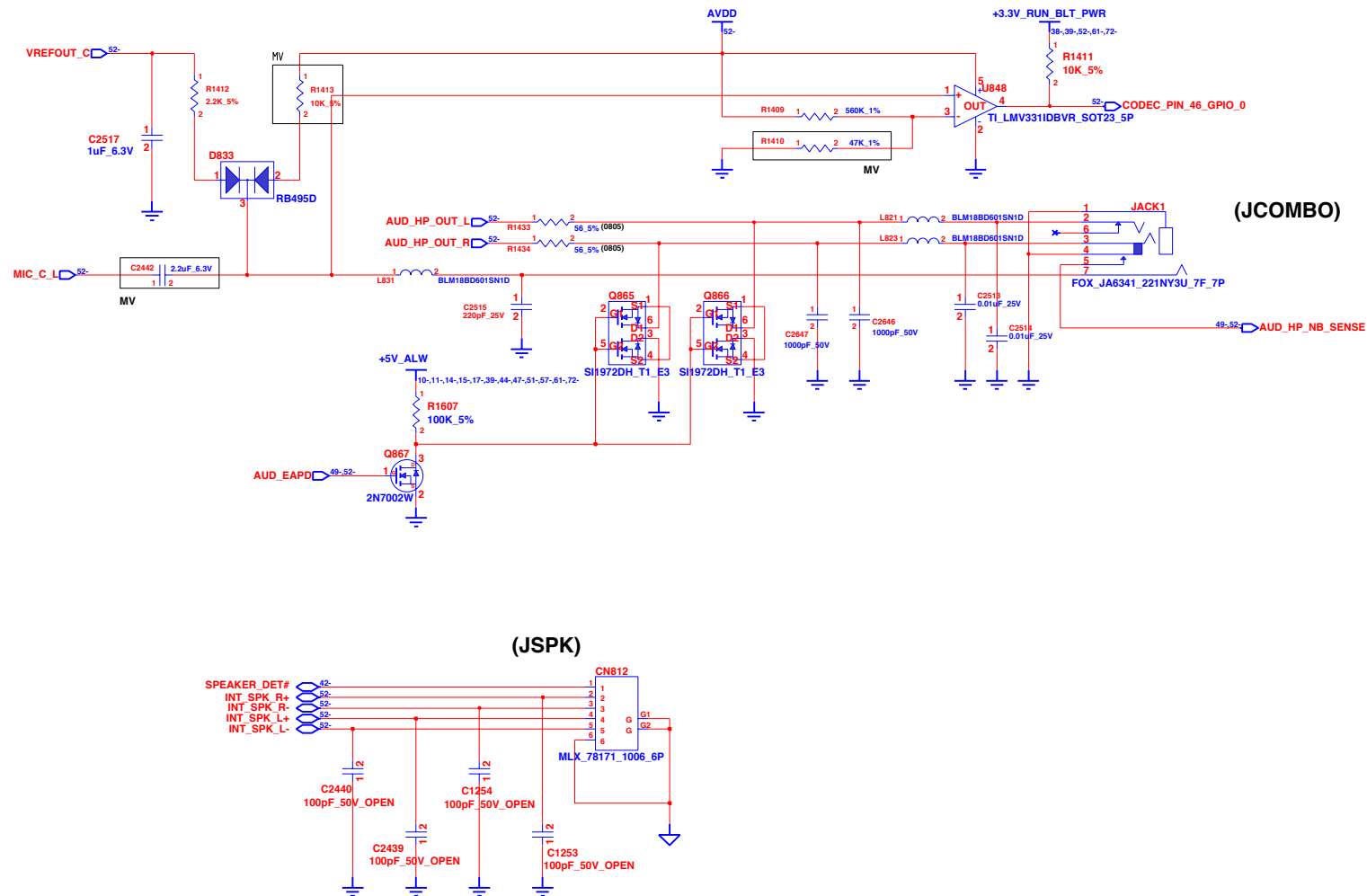
CHANGE by **Drawer Name** 23-Apr-2009

8



CHANGE by	<i>Drawer Name</i>	23-Apr-2009
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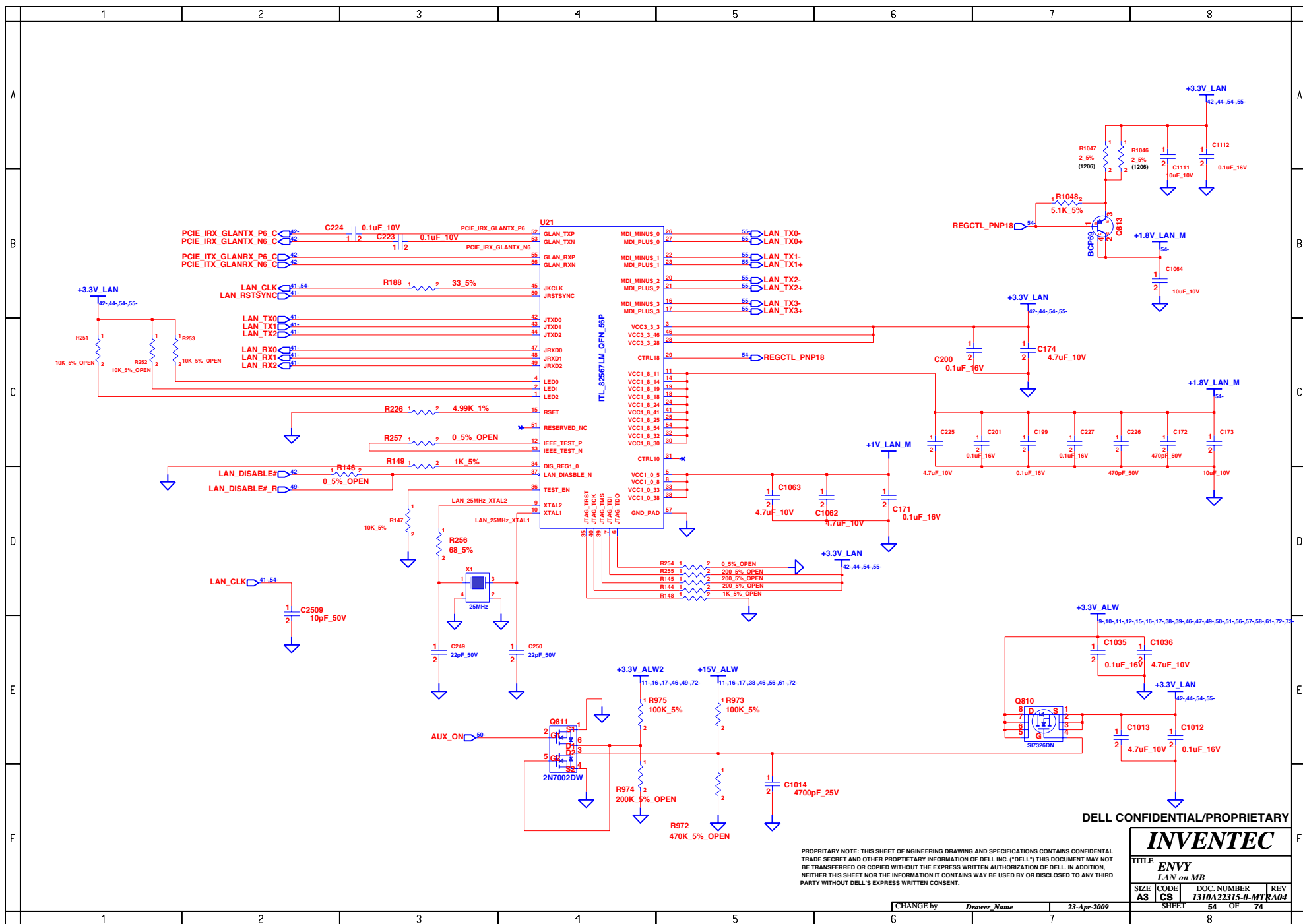


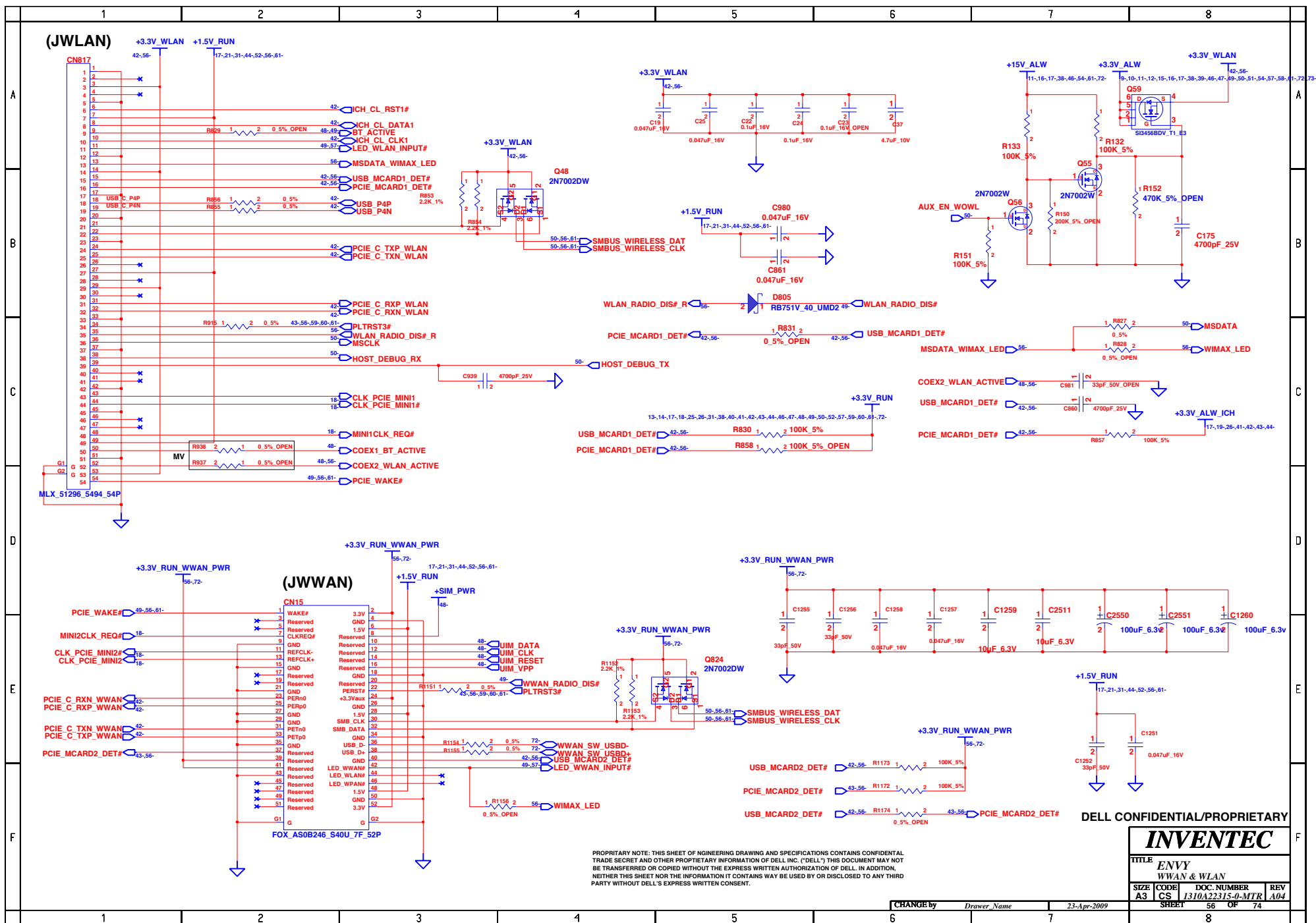


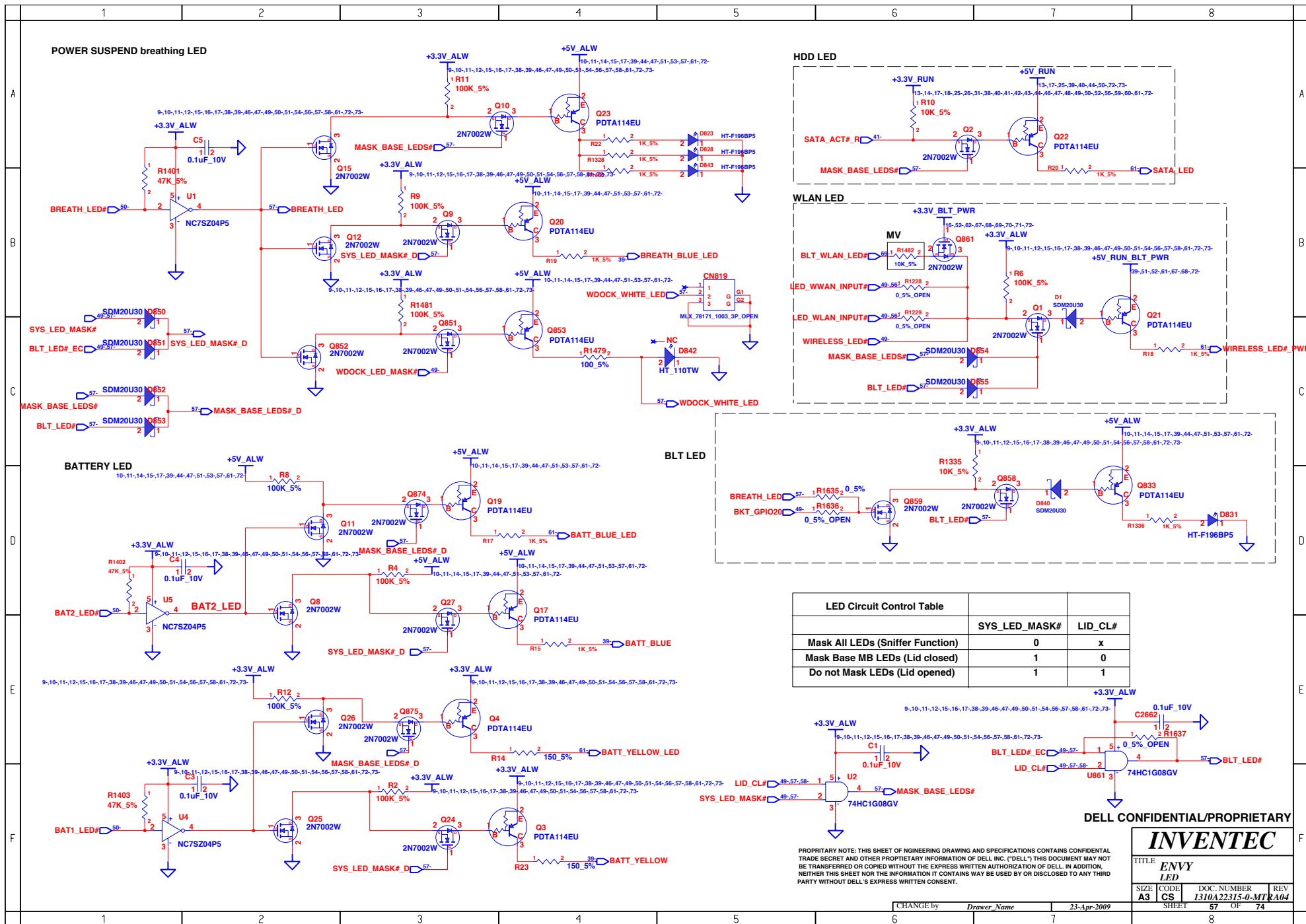
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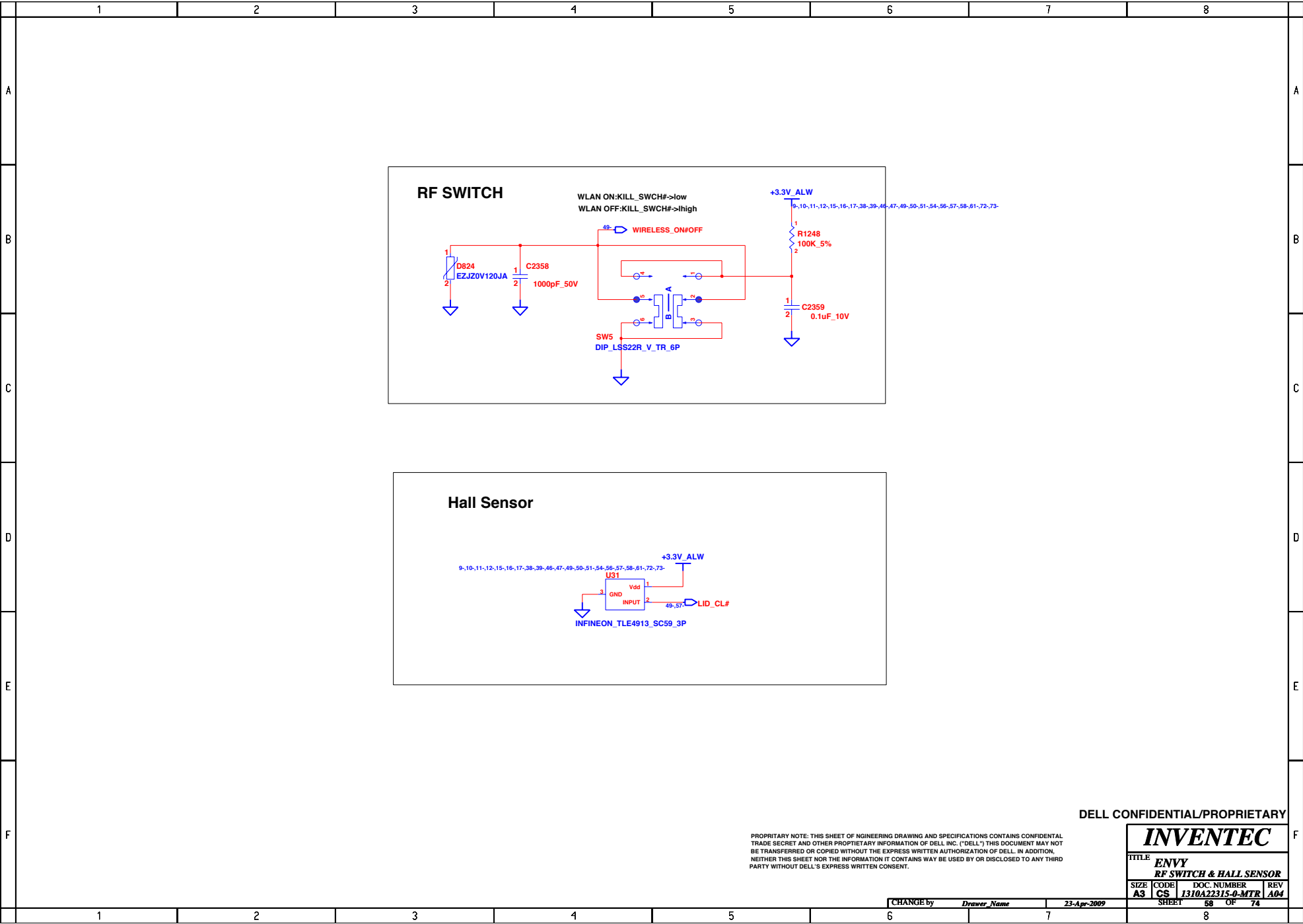
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INVENTEC			
TITLE: ENVY AUDIO AMP & MIC & SPEAKER			
SIZE: A3	CODE: CS	DOC. NUMBER: 1310A22315-0-MTR	REV: A04
CHANGE by: Drawer Name		23-Apr-2009	
SHEET: 53		OF: 74	







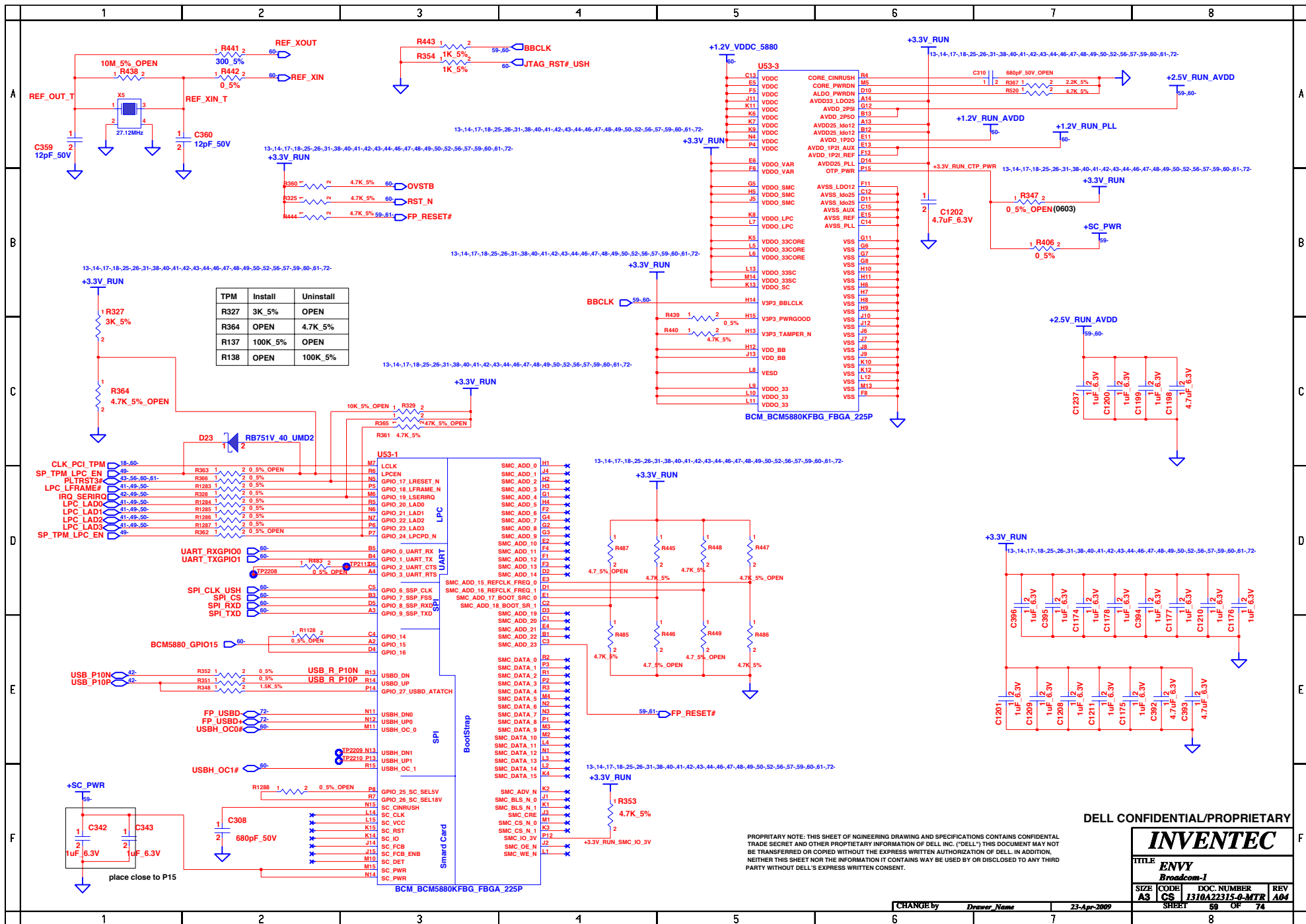


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INVENTEC			
TITLE ENVY RF SWITCH & HALL SENSOR			
SIZE A3	CODE CS	DOC. NUMBER 1310A22315-0-MTR	REV A04
SHEET 58		OF 74	

CHANGE by Drawer_Name 23-Apr-2009



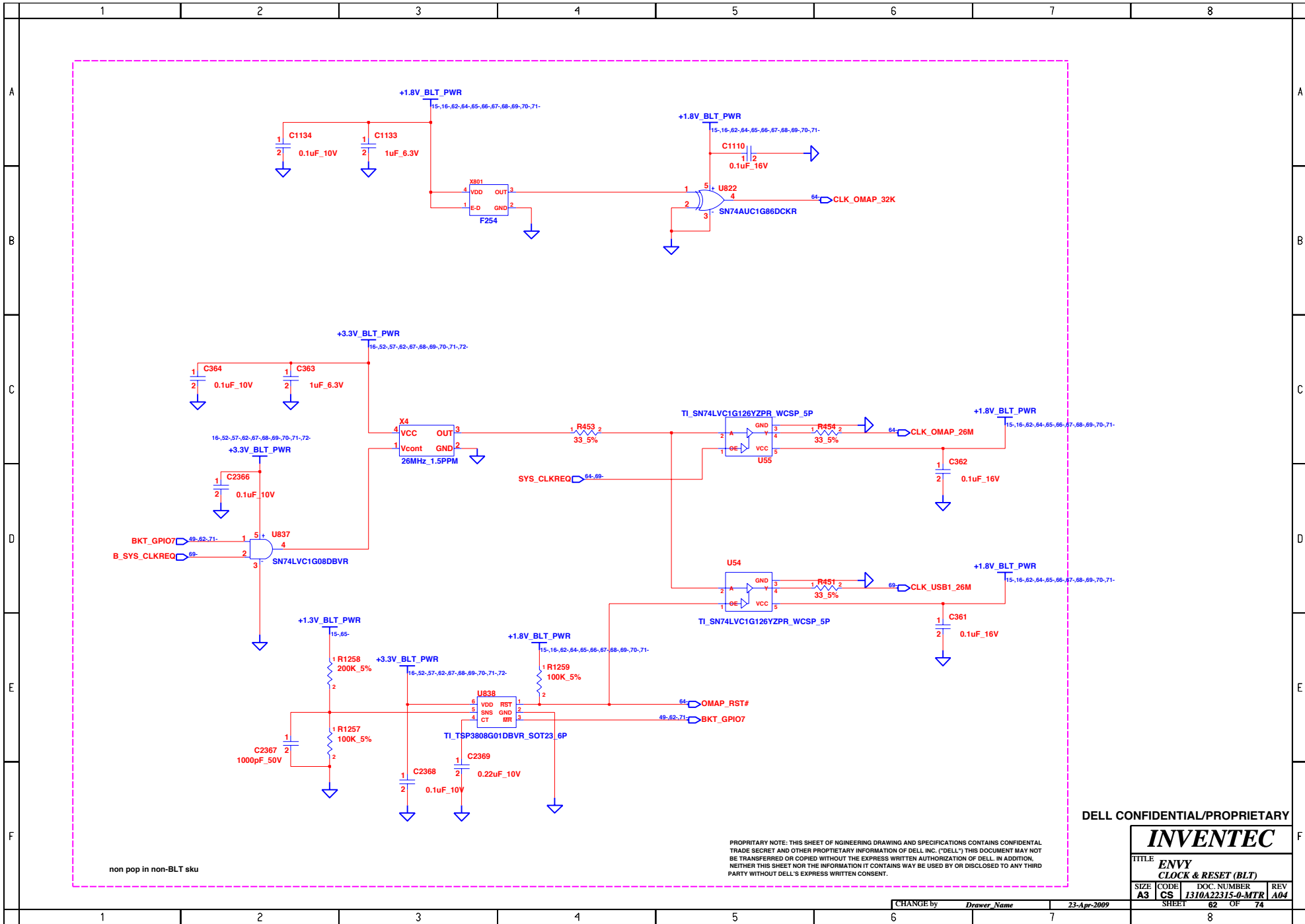
DELL CONFIDENTIAL/PROPRIETARY

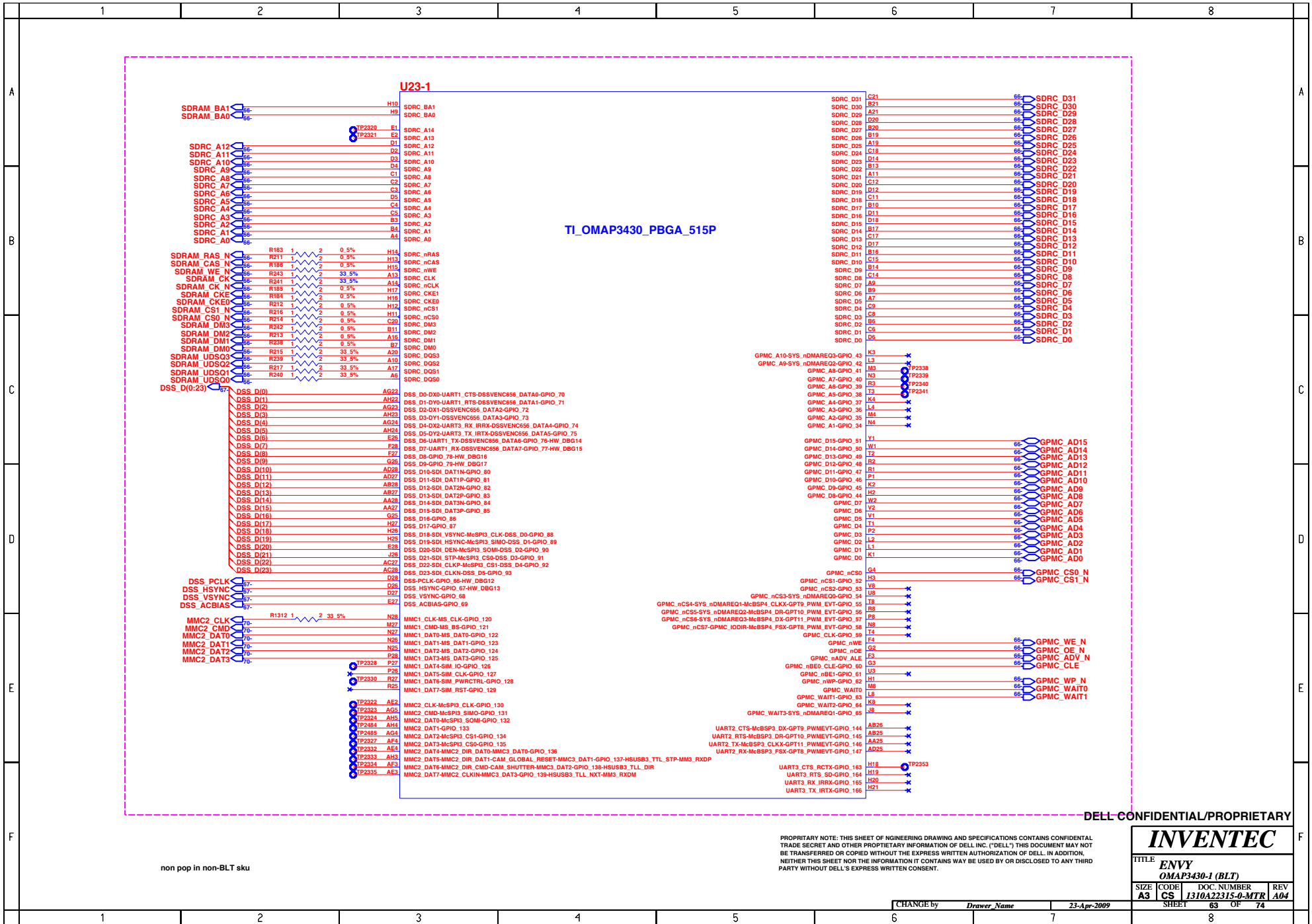
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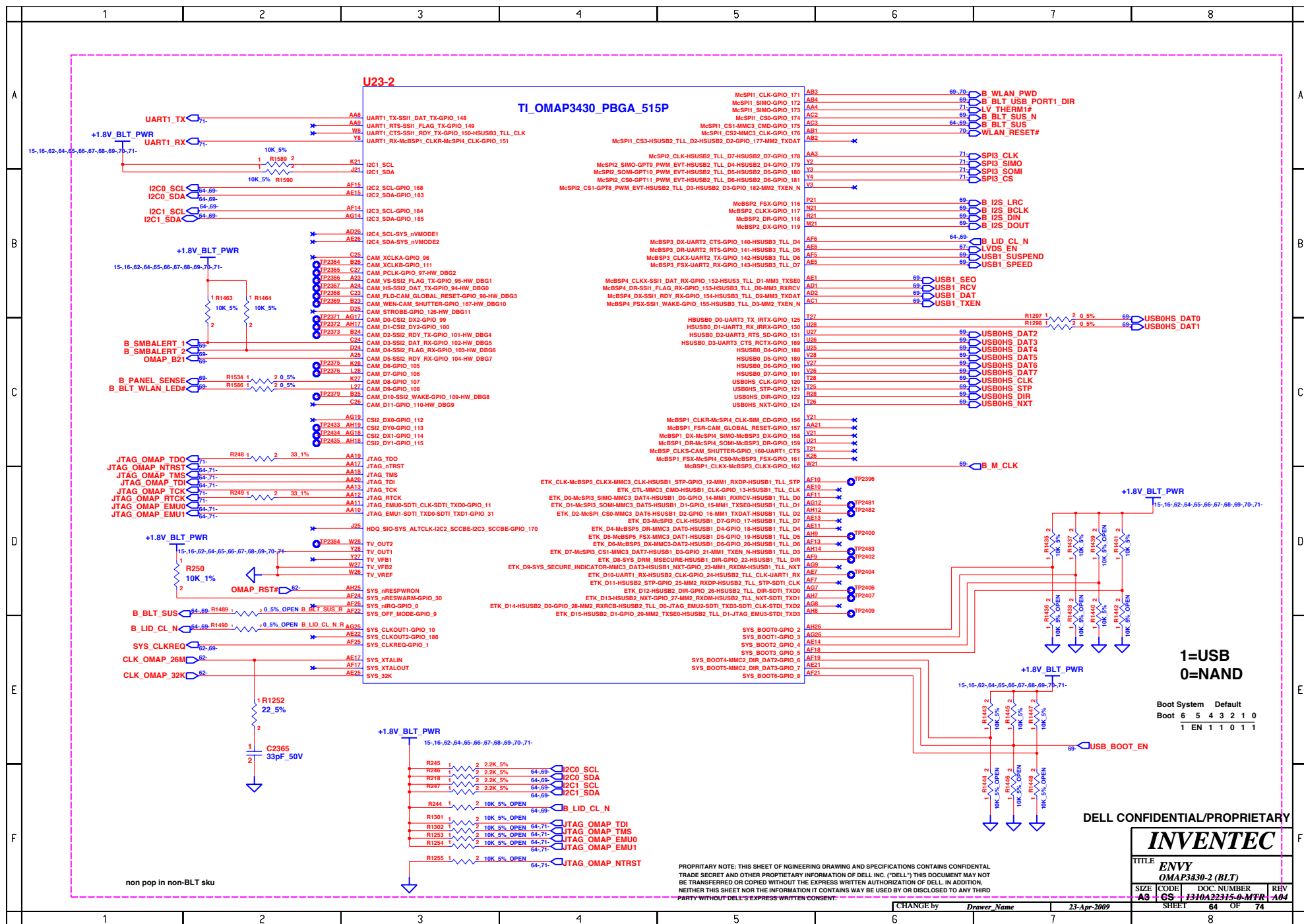
TITLE ENVY <i>Broadcom-1</i>			
SIZE A3	CODE CS	DOC. NUMBER 1310A22315-0-MTR	REV A04
SHEET 59		OF 74	

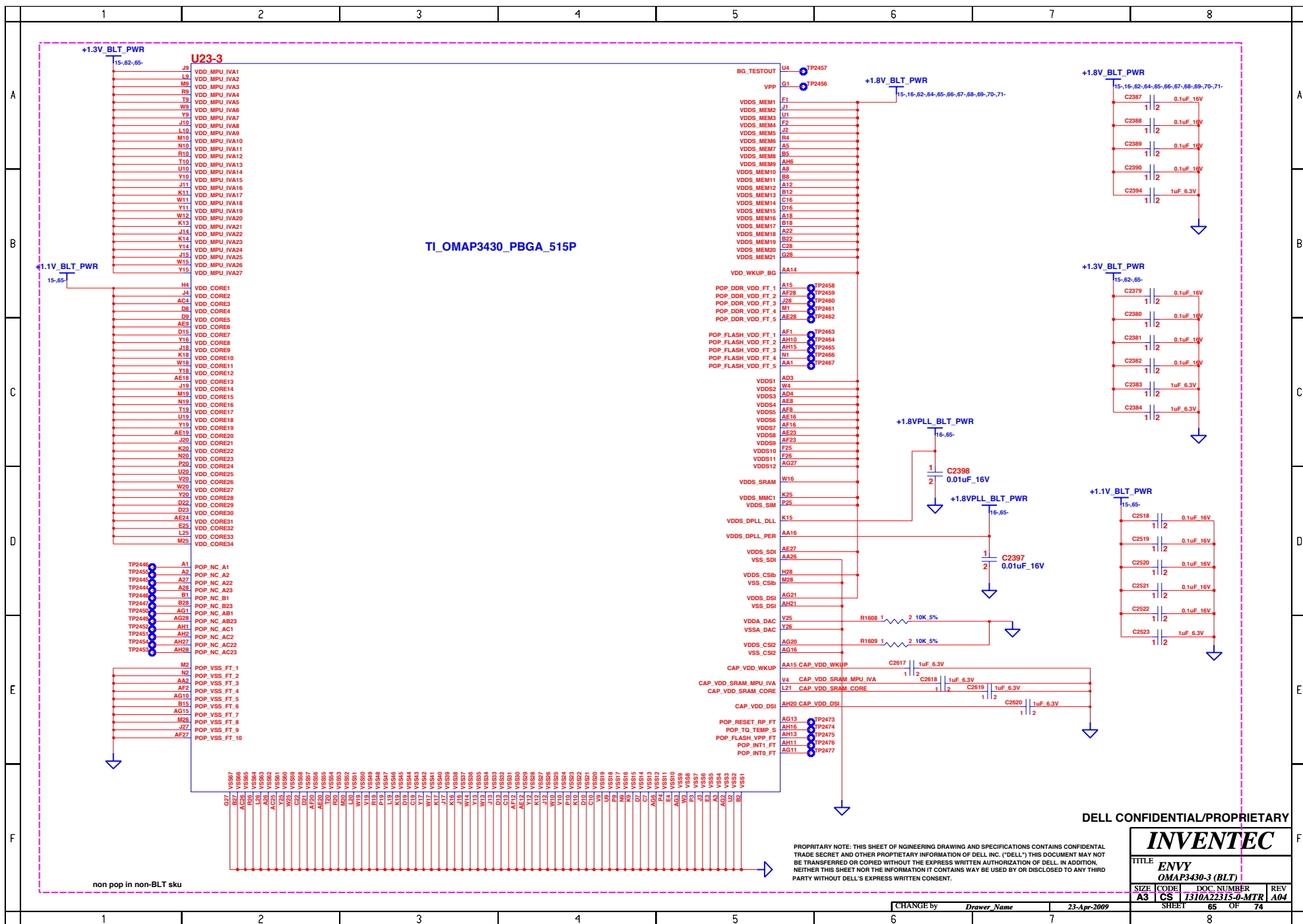
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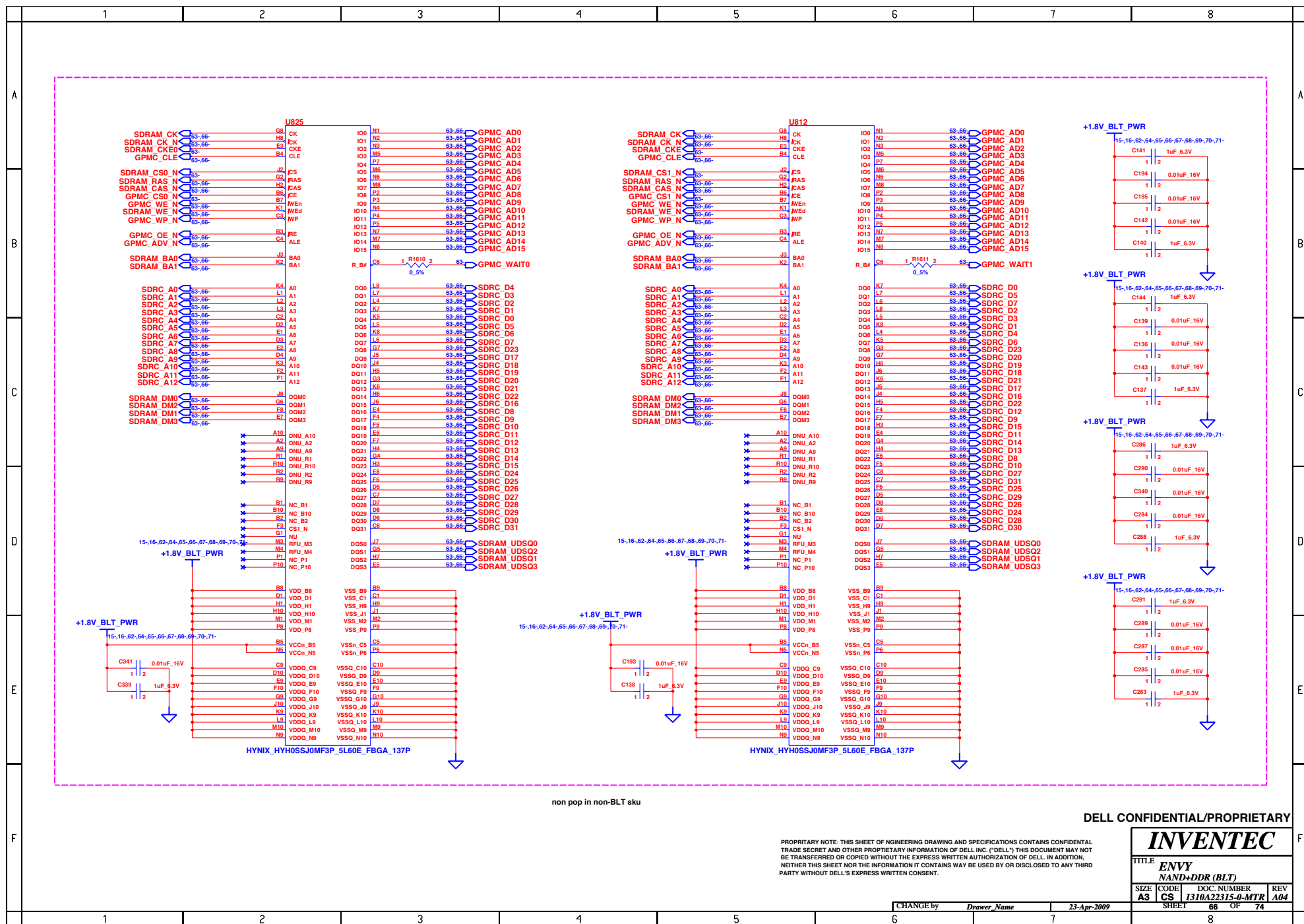
CHANGE by *Dwyer Name* 23-Apr-2009

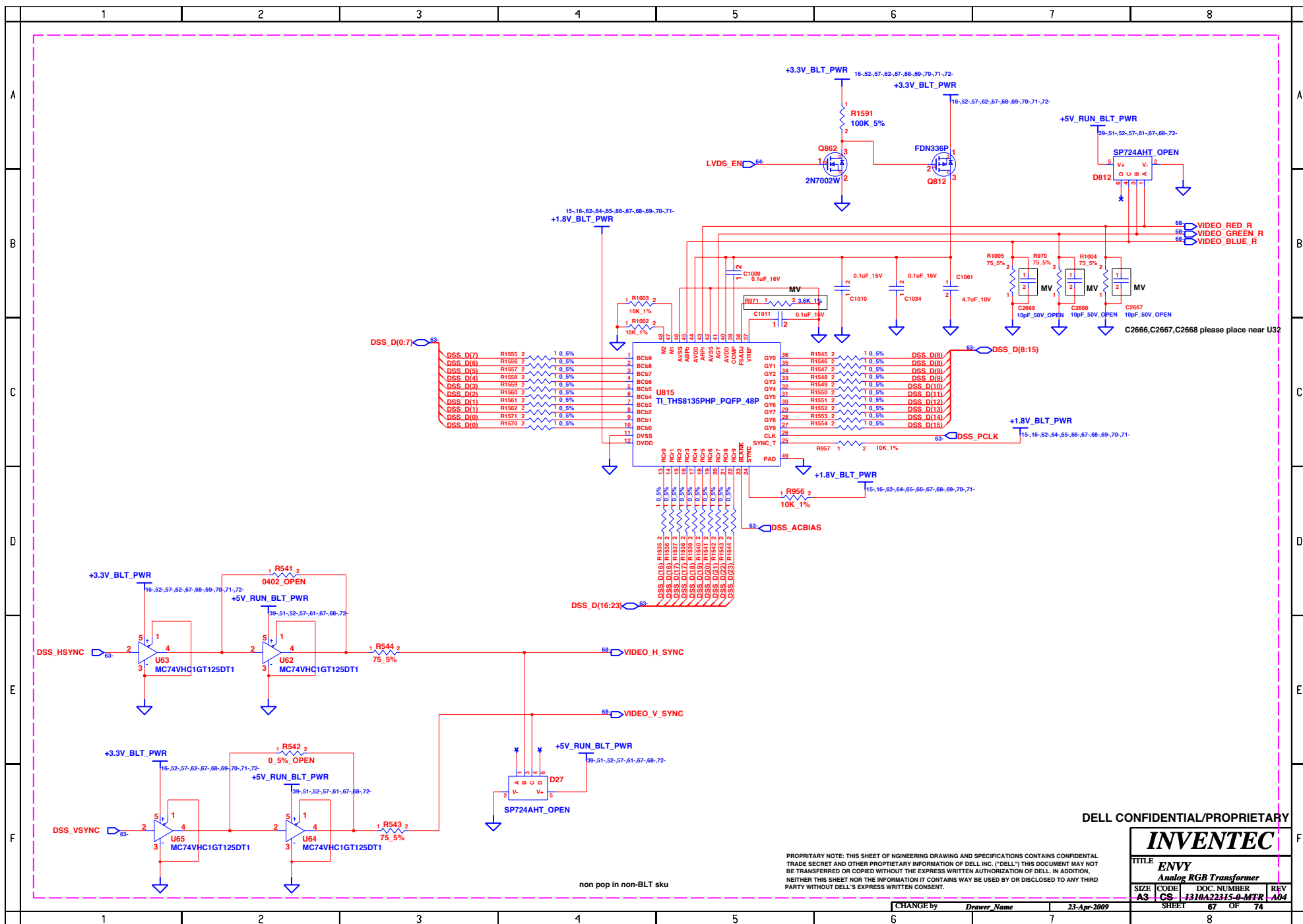


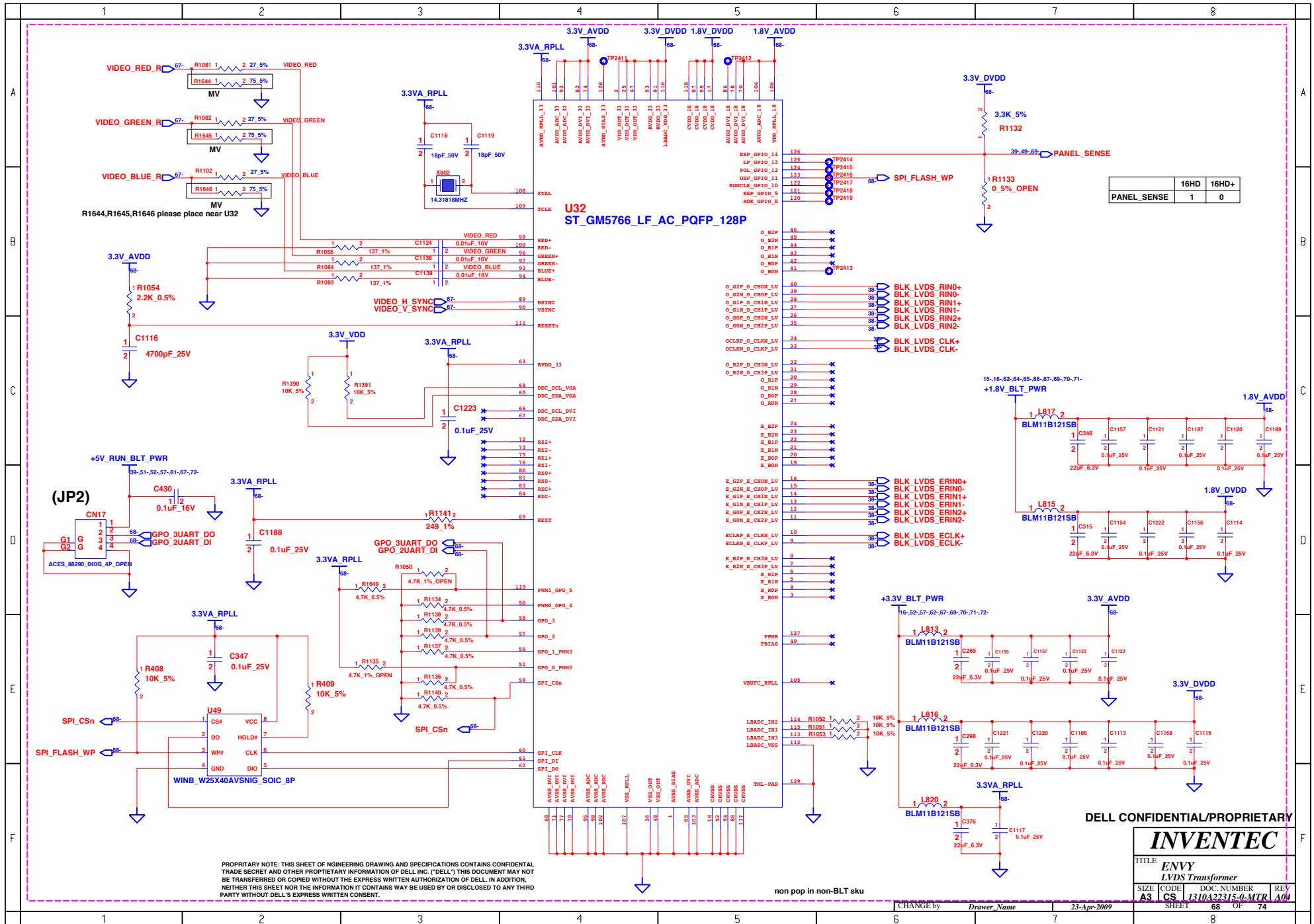












	16HD	16HD+
PANEL_SENSE	1	0

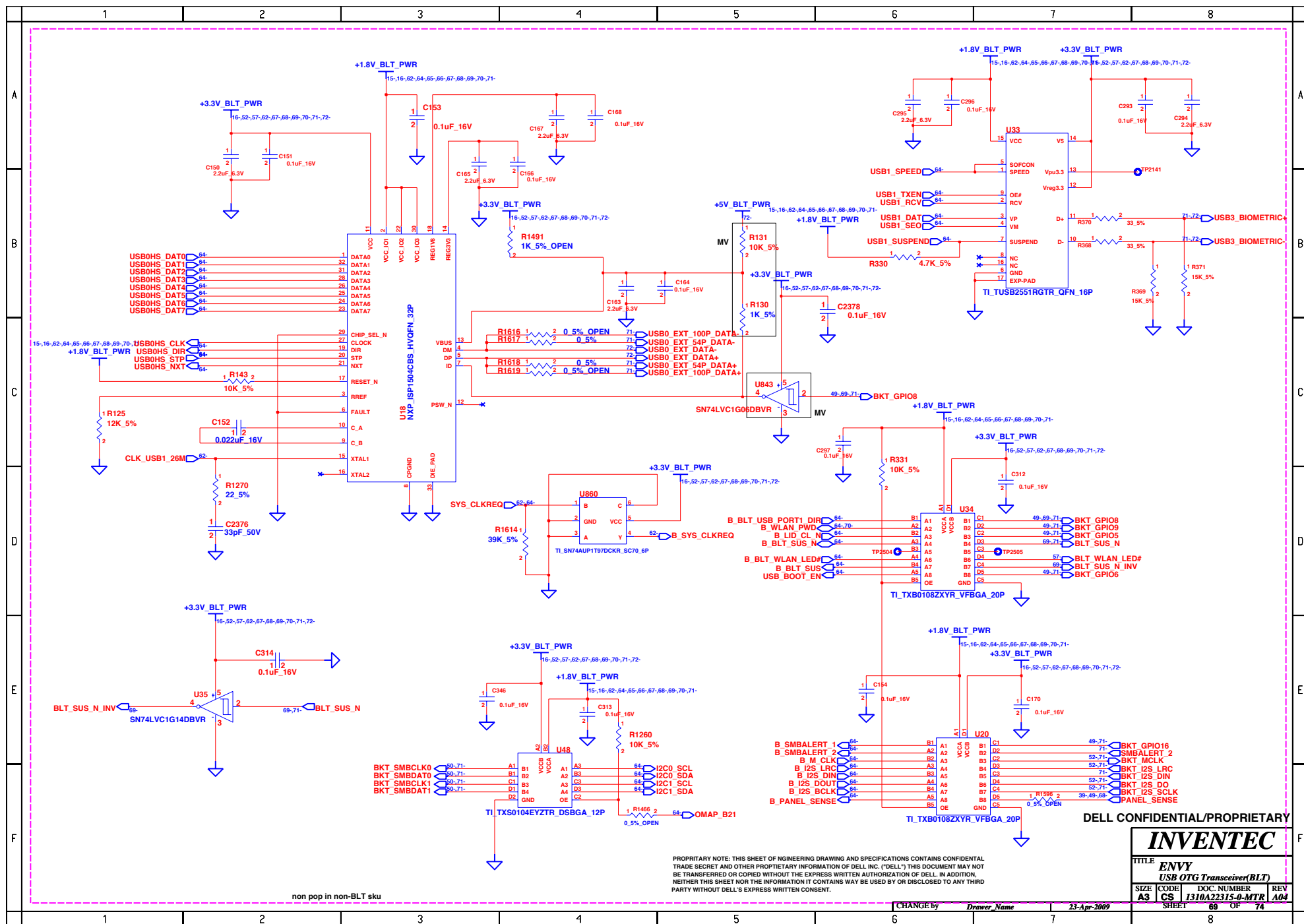
DELL CONFIDENTIAL/PROPRIETARY

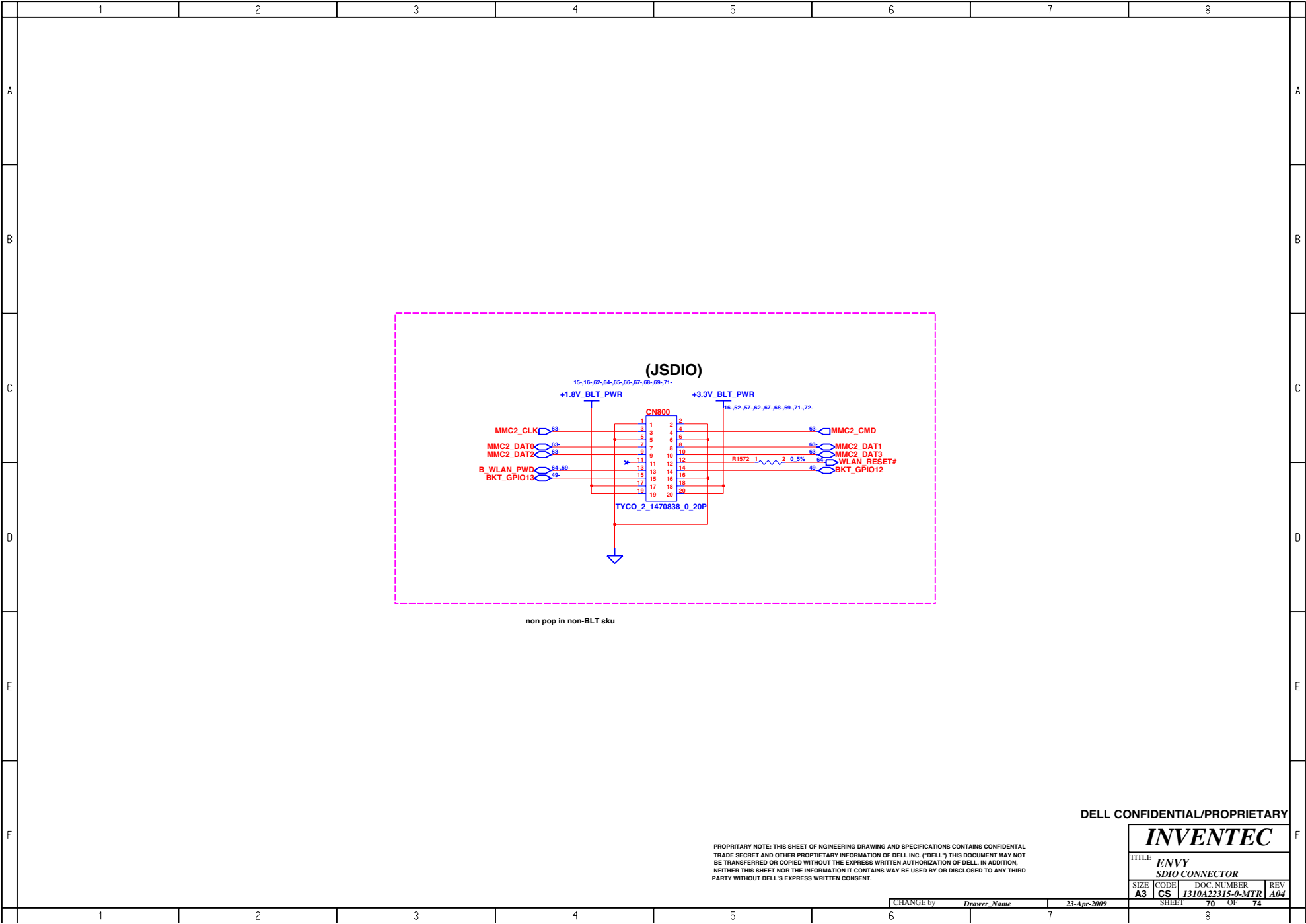
INVENTEC

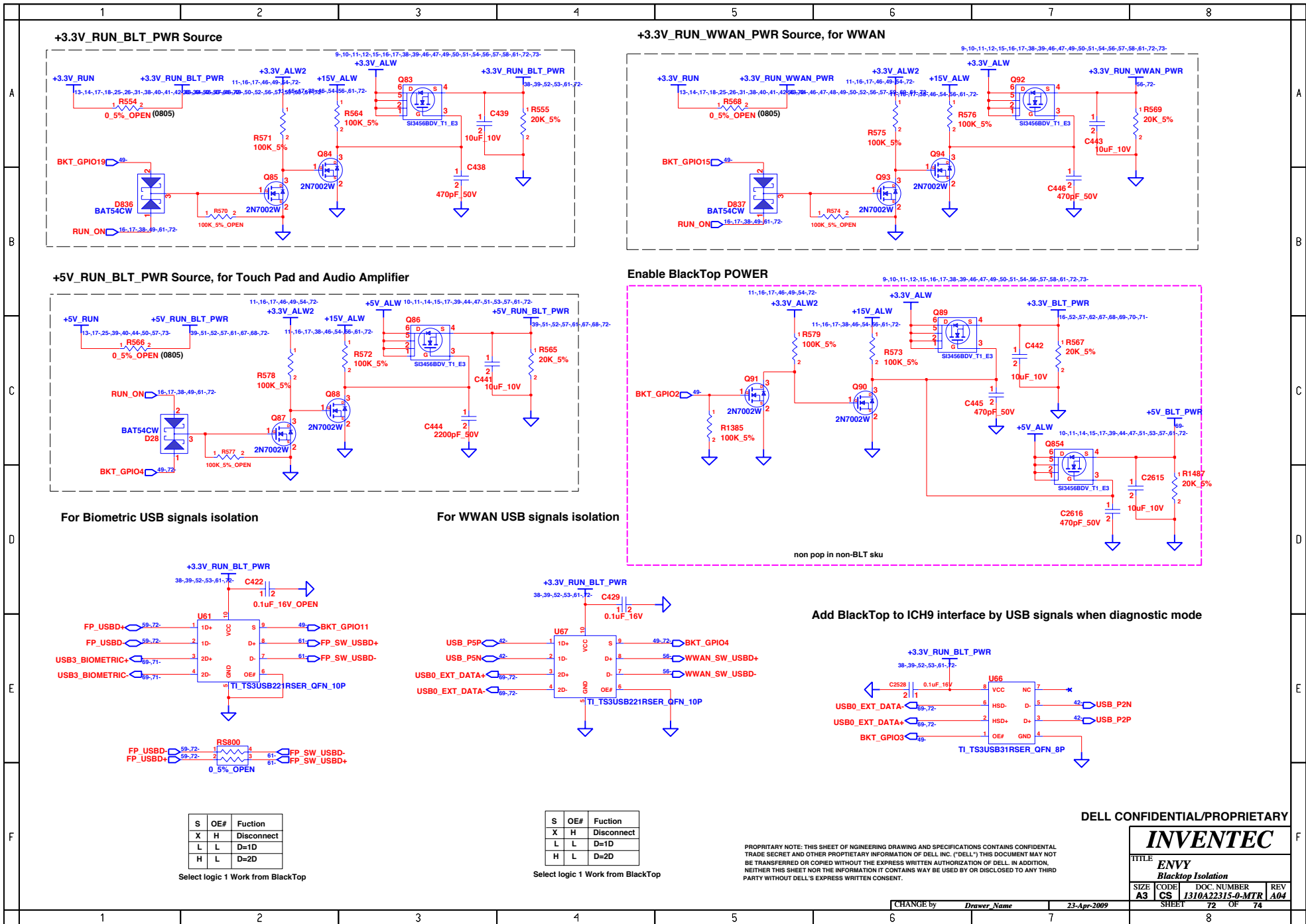
TITLE			
ENVY LVDS Transformer			
SIZE	CODE	DOC. NUMBER	REV
A3	CS	1310A22315-0-MTR	A04
SHEET		68	OF 74

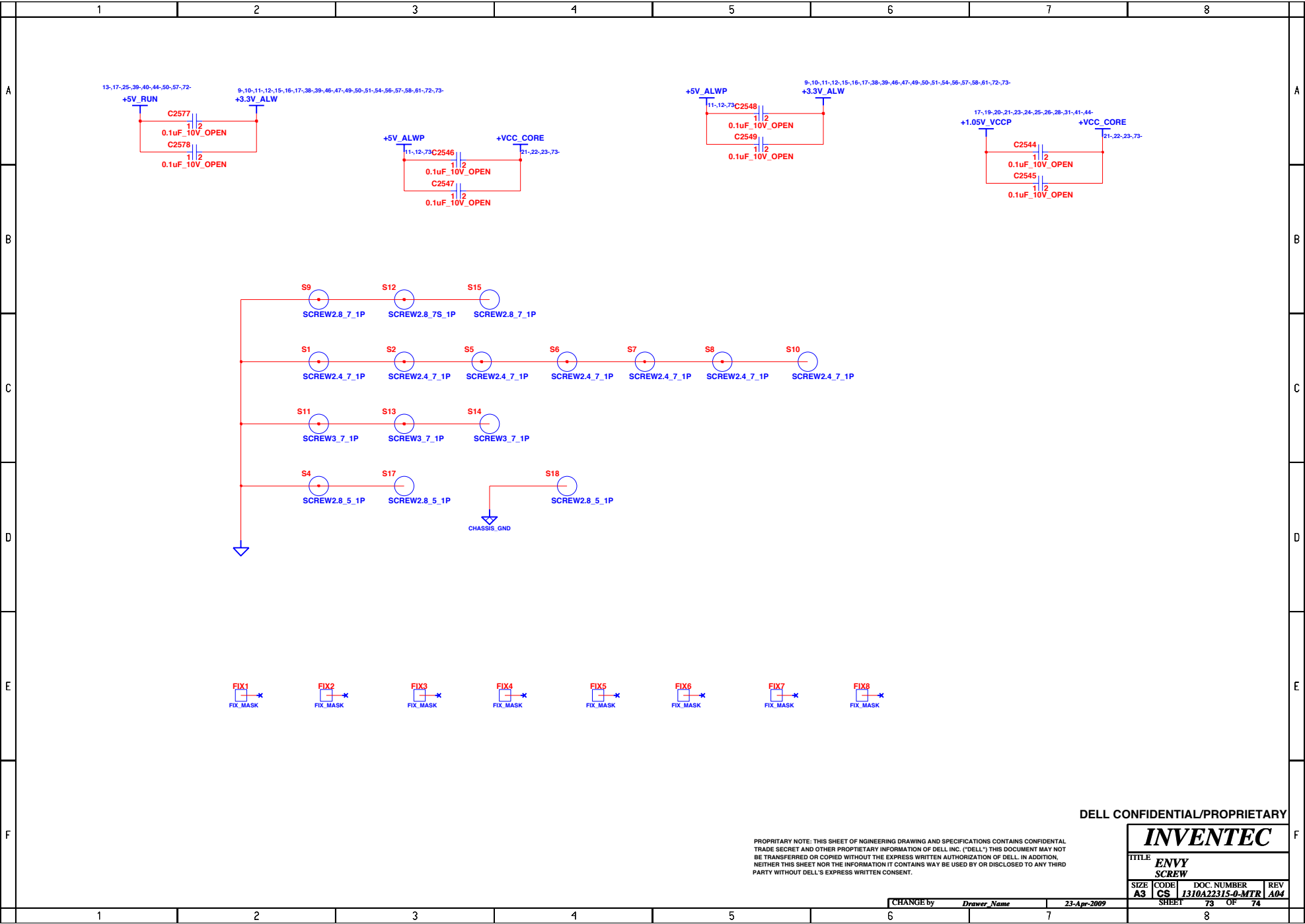
non pop in non-BLT sku

CHANGE by	Drawn Name	23-Apr-2009
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INVENTEC			
TITLE ENVY SCREW			
SIZE A3	CODE CS	DOC. NUMBER 1310A22315-0-MTR	REV A04
SHEET 73		OF 74	

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CHANGE by *Drawer_Name* 23-Apr-2009

	1	2	3	4	5	6	7	8																	
A	Bootling Sequence when SYS.BOOT[5] = 0 Memory Booting perferred				Bootling Sequence when SYS.BOOT[5] = 1 Peripheral Booting perferred				SYS.BOOT [4:0]	A															
	1st	2nd	3rd	4th	1st	2nd	3rd	4th																	
	OneNAND	SSI	USB		SSI	USB	OneNAND		00000b 0																
	NAND	SSI	USB		SSI	USB	NAND		00001b 1																
	OneNAND	SSI	USB	MMC1	SSI	USB	MMC1	OneNAND	00010b 2																
	MMC2	SSI	USB	MMC1	SSI	USB	MMC1	MMC2	00011b 3																
B	OneNAND	USB			USB	OneNAND			00100b 4	B															
	MMC2	USB			USB	MMC2			00101b 5																
	MMC1	USB			USB	MMC1			00110b 6																
	XIP	SSI	USB		SSI	USB	XIP		00111b 7																
	XDOC	SSI	USB		SSI	USB	XDOC		01000b 8																
	MMC2	SSI	USB		SSI	USB	MMC2		01001b 9																
C	XIP	SSI	USB	MMC1	SSI	USB	MMC1	XIP	01010b 10	C															
	XDOC	SSI	USB	MMC1	SSI	USB	MMC1	XDOC	01011b 11																
	NAND	SSI	USB	MMC1	SSI	USB	MMC1	NAND	01100b 12																
	XIP	USB	UART	MMC1	USB	UART	MMC1	XIP	01101b 13																
	XDOC	USB	UART	MMC1	USB	UART	MMC1	XDOC	01110b 14																
	NAND	USB	UART	MMC1	USB	UART	MMC1	NAND	01111b 15																
D	OneNAND	USB	UART	MMC1	USB	UART	MMC1	OnENAND	10000b 16	D															
	MMC2	USB	UART	MMC1	USB	UART	MMC1	MMC2	10001b 17																
	MMC1	USB	UART		USB	UART	MMC1		10010b 18																
	XIP	UART			UART	XIP			10011b 19																
	XDOC	UART			UART	XDOC			10100b 20																
	NAND	UART			UART	NAND			10101b 21																
	OneNAND	UART			UART	OneNAND			10110b 22																
E	MMC2	UART			UART	MMC2			10111b 23	E															
	MMC1	UART			UART	MMC1			11000b 24																
	XIP	USB			USB	XIP			11001b 25																
	XDOC	USB			USB	SDOC			11010b 26																
	NAND	USB			USB	NAND			11011b 27																
	reserved				reserved				11100b 28																
	reserved				reserved				11101b 29																
F	reserved for fabrication test purposes				reserved for fabrication test purposes				11110b 30	F															
	see Table 11 for details				see Table 11 for details				11111b 31																
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<table><tr><td colspan="4">INVENTEC</td></tr><tr><td colspan="4">TITLE ENVY SYSTEM BOOT DEFULT TABLE</td></tr><tr><td>SIZE A3</td><td>CODE CS</td><td>DOC. NUMBER I310A22315-0-MTR</td><td>REV A04</td></tr><tr><td colspan="2">SHEET 74</td><td colspan="2">OF 74</td></tr></table>										INVENTEC				TITLE ENVY SYSTEM BOOT DEFULT TABLE				SIZE A3	CODE CS	DOC. NUMBER I310A22315-0-MTR	REV A04	SHEET 74		OF 74	
INVENTEC																									
TITLE ENVY SYSTEM BOOT DEFULT TABLE																									
SIZE A3	CODE CS	DOC. NUMBER I310A22315-0-MTR	REV A04																						
SHEET 74		OF 74																							
	1	2	3	4	5	6	7	8																	
CHANGE by						Drawer Name	23-Apr-2009																		

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INVENTEC			
TITLE: ENVY SYSTEM BOOT DEFULT TABLE			
SIZE A3	CODE CS	DOC. NUMBER 1310A22315-0-MTR	REV A04
SHEET		74	OF 74

CHANGE by *Drawer Name* 23-Apr-2009

	1	2	3	4	5	6	7	8	
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